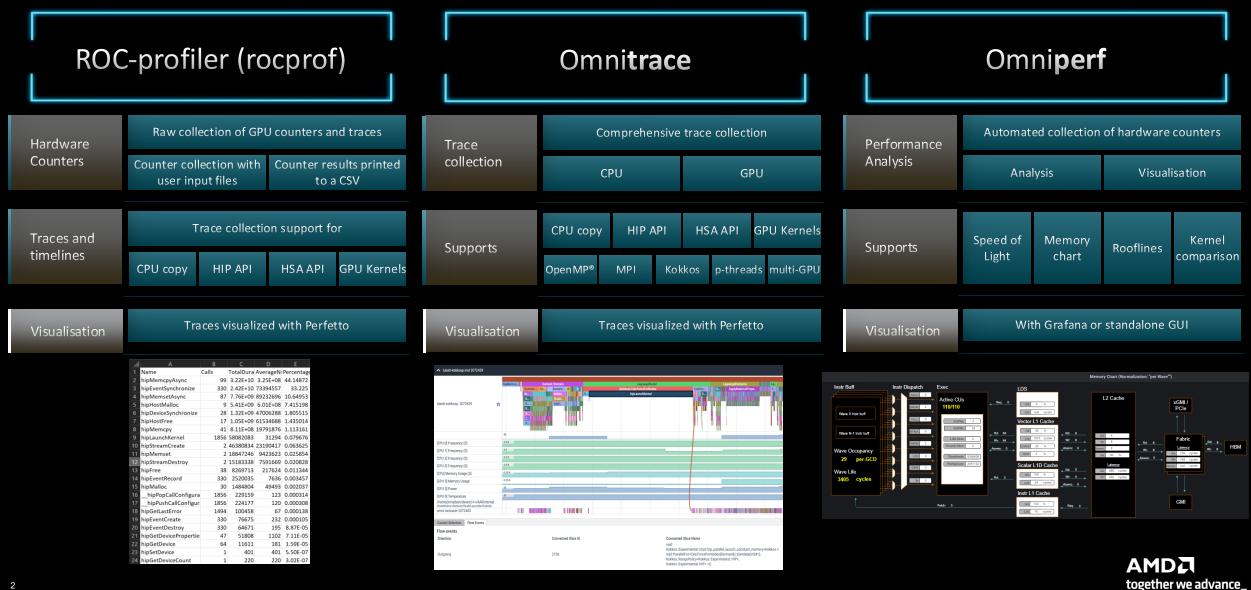
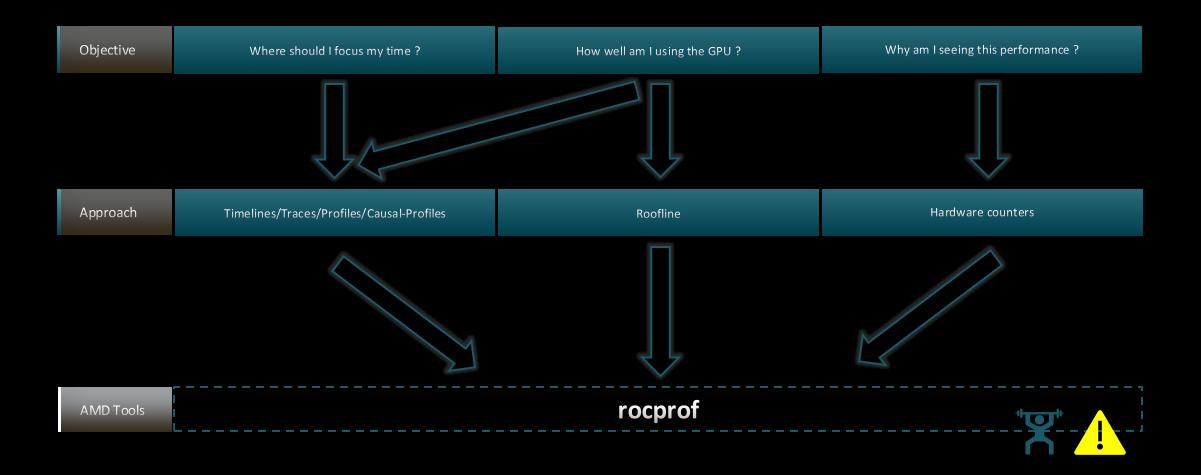


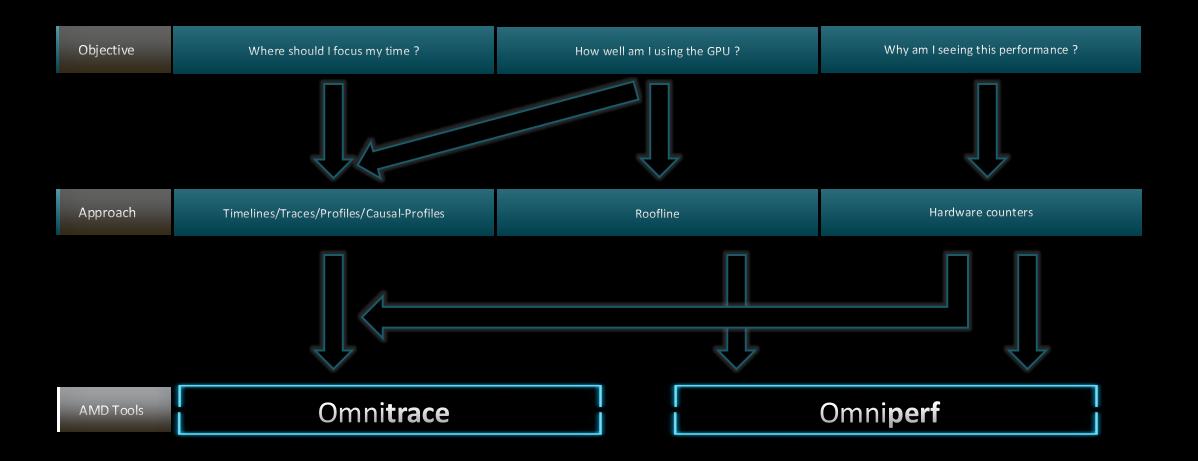
GPU Timeline Profiling

Presenter: Sam Antao LUMI Pre-hackathon training Oct 8th, 2024

> AMD together we advance_





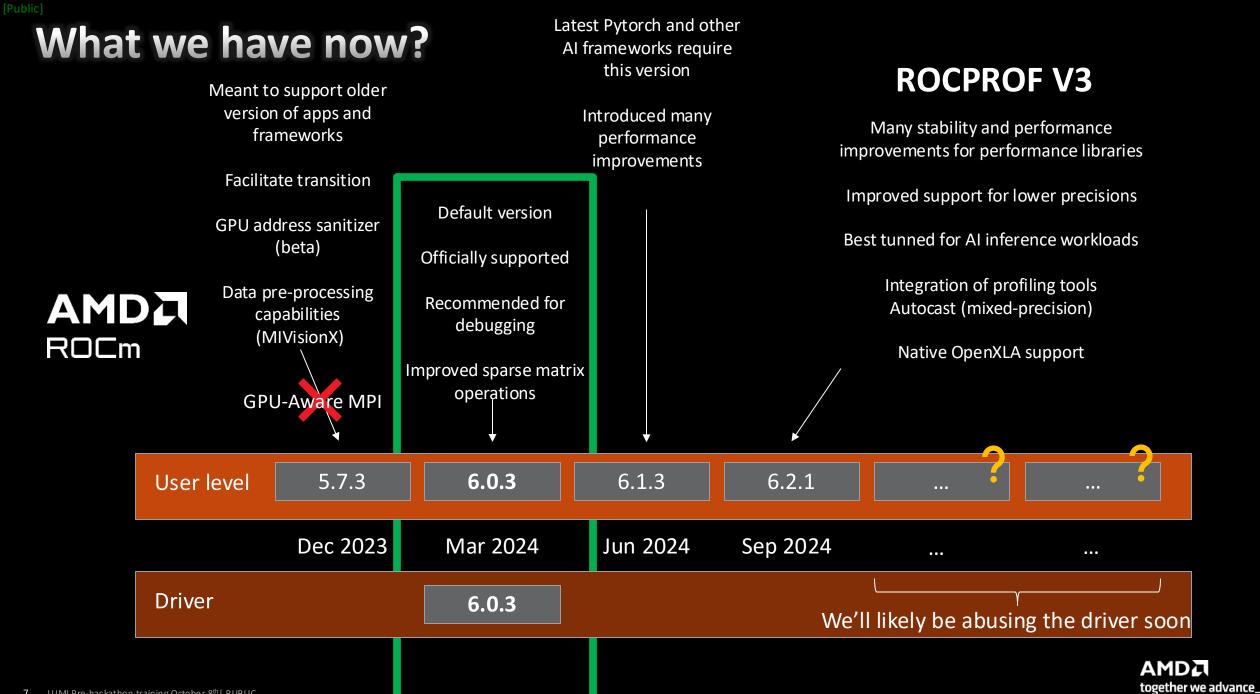


ROC-profiler (rocprof))	Omni trace			J	Omni perf						
Hardware	Raw collection of	GPU counters an	id traces	Trace	Compr	ehensive tra	ce collection		Performance	Automated collection of hardware counter			re counters	
	Counter collection w user input files		ults printed CSV	collection	CPU		GPU		Analysis	An	Analysis		Visualization	
Traces and timelines	Trace coll	ection support fo	r	Supports	CPU copy	HP API F	ISA API GPU Keri	nels	Supports	Speed of	Memory	Rooflines	s Kernel	
	CPU copy HIP A	PI HSA API	GPU Kernels		Open MP® MPI Kokkos		p-threads multi-	GPU	PU		Light chart		compari	
Visualisation	Traces visua	alized with Perfet	to	Visualisation	Traces	s visualized w	ith Perfetto		Visualisation	W	/ith Grafana (or standalon	e GUI	
	hjpKmercpyAsync 99 3.22 hjpEventSynchronize 330 2.42E hjpKmestSkavnc 87 7.76E hjpHostMalloc 9 5.12E hjpHostMalloc 9 4.12E hjpHostMalloc 9 4.12E hjpHostFree 17 1.05E hjpKmerpy 41 8.11E hjpLauchKernel 1856 58082 hjpKuenset 2 18347 jhjpKenrepy 15183 3 jhpFore 38 8269 jhjpKuencord 330 2520 jhjpKallocofigura 1856 224 jhjpPopCallConfigura 1856 224 jhjpMushCallConfigura 1856 224 jhjpSushCallConfigura 1856 224 jhjpSushCallConfigura 1856 224 jhjpSushCallConfigura 1856 224 jhjpGetallConfigura 1856 224	334 23190417 0.063625 46 9423623 0.025884 38 7591660 0.020828 13 217624 0.011344 35 0.030857 0.002037 46 49493 0.002037 159 123 0.00314 177 120 0.00308 56 67 0.000138						Viewe Occu Wave Occu 3405 c	we total and the second		Image: Section 1 Section 1	Memory Chart (Normalization: "per V L2 Cache L2 Cache Latery Latery Latery Latery Latery	Mave") XCMI / PCIo Fabric Lates: W * * CMI	
22	0 hipEventDestroy 330 64/ 1 hipGetDevicePropertie 47 51: 2 hipGetDevice 64 11: 3 hipSetDevice 1 1	71 195 8.87E-05 808 1102 7.11E-05		Current Selection Prove Dents Prove vents Direction Cutgoing	Connected Slice ID 2736	mpt:ParallelFor <calcfo< td=""><td>ngl-hja zavalid Jauch, constant memory-Kokkor I rede"nikodi (Samini) (lambda[n1]41), silo : Experimental He>,</td><td></td><td></td><td></td><td></td><td></td><td></td></calcfo<>	ngl-hja zavalid Jauch, constant memory-Kokkor I rede"nikodi (Samini) (lambda[n1]41), silo : Experimental He>,							

Introduction to ROC-Profiler

Presenter: Sam Antao LUMI Pre-hackathon training October 8th, 2024

> AMD together we advance_



What is ROC-Profiler (v1-v2-v3)?

- ROC-profiler (also referred to as **rocprof**) is the command line front-end for AMD's GPU profiling libraries
 - Repo: <u>https://github.com/ROCm-Developer-Tools/rocprofiler</u>
- rocprof contains the central components allowing application traces and counter collection
 - Under constant development
- Distributed with ROCm
- The output of rocprofv1 can be visualized in the Chrome browser with Perfetto (<u>https://ui.perfetto.dev/</u>)
- There are ROCProfiler V1 and V2 (roctracer and rocprofiler into single library, same API)
- ROC-profiler-SDK is a profiling and tracing library for HIP and ROCm application. The new API improved thread safety and includes more efficient implementations and provides a tool library to support on writing your tool implementations. It is still in beta release.
- rocprofv3 uses this tool library to profile and trace applications.

[Public]

rocprof (v1): Getting Started + Useful Flags

 To get help: \${ROCM_PATH}/bin/rocprof -h

- Useful housekeeping flags:
 - --timestamp <on|off> turn on/off gpu kernel timestamps
 - --basenames <on|off> turn on/off truncating gpu kernel names (i.e., removing template parameters and argument types)
 - -o <output csv file> Direct counter information to a particular file name
 - -d <data directory> Send profiling data to a particular directory
 - -t <temporary directory> Change the directory where data files typically created in /tmp are placed. This allows you to save these temporary files.
- Flags directing rocprofiler activity:
 - -i input<.txt|.xml> specify an input file (note the output files will now be named input.*)
 - --hsa-trace to trace GPU Kernels, host HSA events (more later) and HIP memory copies.
 - --hip-trace to trace HIP API calls
 - --roctx-trace to trace roctx markers
 - --kfd-trace to trace GPU driver calls
- Advanced usage
 - -m <metric file> Allows the user to define and collect custom metrics. See <u>rocprofiler/test/tool/*.xml</u> on GitHub for examples.

- rocprof can collect kernel(s) execution stats
 - \$ /opt/rocm/bin/rocprof --stats --basenames on <app with arguments>
- This will output two csv files:
 - results.csv: information per each call of the kernel
 - results.stats.csv: statistics grouped by each kernel
- Content of results.stats.csv to see the list of GPU kernels with their durations and percentage of total GPU time:

"Name","Calls","TotalDurationNs","AverageNs","Percentage"
"JacobiIterationKernel",1000,556699359,556699,43.291753895270446
"NormKernel1",1001,430797387,430367,33.500980655394606
"LocalLaplacianKernel",1000,280014065,280014,21.775307970480817
"HaloLaplacianKernel",1000,14635177,14635,1.1381052818810995
"NormKernel2",1001,3770718,3766,0.2932300765671734
"__amd_rocclr_fillBufferAligned.kd",1,8000,8000,0.0006221204058583505

In a spreadsheet viewer, it is easier to read:

	A	В	С	D	E
1	Name	Calls	TotalDurationNs	AverageNs	Percentage
2	JacobiIterationKernel	1000	556699359	556699	43.2917538952704
3	NormKernel1	1001	430797387	430367	33.5009806553946
4	LocalLaplacianKernel	1000	280014065	280014	21.7753079704808
5	HaloLaplacianKernel	1000	14635177	14635	1.1381052818811
6	NormKernel2	1001	3770718	3766	0.293230076567173
7	amd rocclr fillBufferAligned	1	8000	8000	0.000622120405858

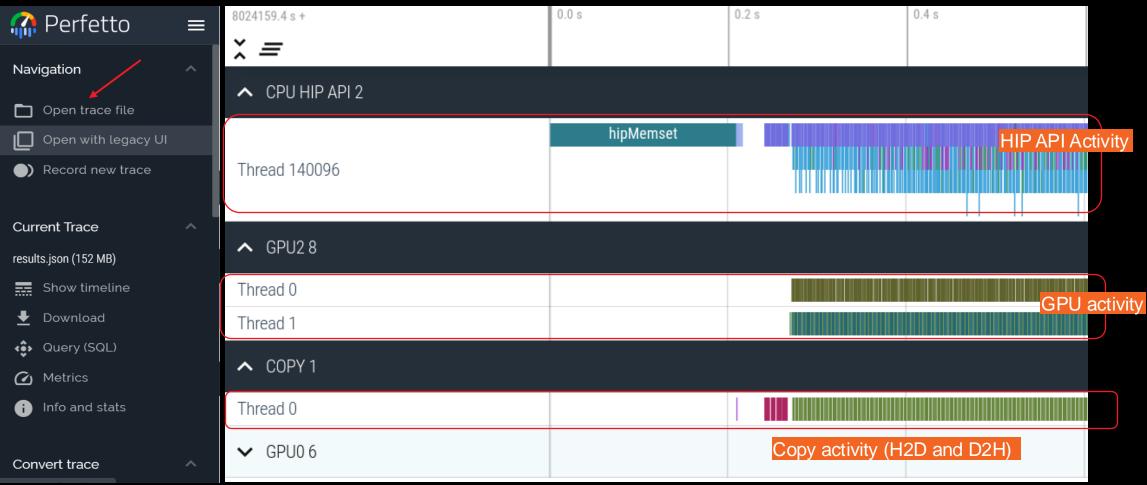
[Public]

rocprof (v1): + Perfetto: Collecting and Visualizing App Traces

rocprof can collect traces

\$ /opt/rocm/bin/rocprof --hip-trace <app with arguments>

This will output a .json file that can be visualized using the Chrome browser and Perfetto (https://ui.perfetto.dev/)



AMD together we advance_

rocprofv3: Getting Started + Useful Flags

- To get help:
 - \${ROCM_PATH}/bin/rocprofv3 -h
- Useful housekeeping flags:

	1 0 0			
	hip-trace	For	Collecting	HIP Traces (runtime + compiler)
	hip-runtime-trace	For	Collecting	HIP Runtime API Traces
٠	hip-compiler-trace	For	Collecting	HIP Compiler generated code Traces
	marker-trace	For	Collecting	Marker (ROCTx) Traces
	memory-copy-trace	For	Collecting	Memory Copy Traces
	stats	For	Collecting	statistics of enabled tracing types
•	hsa-trace	For	Collecting	HSA Traces (core + amd + image + finalizer)
	hsa-core-trace	For	Collecting	HSA API Traces (core API)
	hsa-amd-trace	For	Collecting	HSA API Traces (AMD-extension API)
	hsa-image-trace	For	Collecting	HSA API Traces (Image-extenson API)
	hsa-finalizer-trace	For	Collecting	HSA API Traces (Finalizer-extension API)

rocprofv3: Getting Started + Useful Flags (II)

```
Useful housekeeping flags:
                           For Collecting HIP, HSA, Marker (ROCTx), Memory copy, Scratch memory, and Kernel
-s, --sys-trace
                                                                                    dispatch traces
• -M, --mangled-kernels Do not demangle the kernel names

    -T, --truncate-kernels Truncate the demangled kernel names

    -L, --list-metrics

                          List metrics for counter collection
• -i INPUT, -- input INPUT Input file for counter collection
• -o OUTPUT FILE, --output-file OUTPUT FILE
                           For the output file name
  -d OUTPUT DIRECTORY, --output-directory OUTPUT DIRECTORY
                           For adding output path where the output files will be saved
• --output-format {csv,json,pftrace} [{csv,json,pftrace} ...]
                           For adding output format (supported formats: csv, json, pftrace)
--log-level {fatal,error,warning,info,trace}
                           Set the log level
--kernel-names KERNEL NAMES [KERNEL_NAMES ...]
                           Filter kernel names

    --preload [PRELOAD ...]

                           Libraries to prepend to LD PRELOAD (usually for sanitizers)

    rocprofv3 requires double-hyphen (--) before the application to be executed, e.g.

    $ rocprofv3 [<rocprofv3-option> ...] -- <application> [<application-arg> ...]
    $ rocprofv3 --hip-trace -- ./myapp -n 1
```

Instructions: https://rocm.docs.amd.com/projects/rocprofiler-sdk/en/docs-6.2.1/how-to/using-rocprofv3.html

- rocprof can collect kernel(s) execution stats
 - \$ /opt/rocm/bin/rocprofv3 --stats --kernel-trace -T -- <app with arguments>
- This will output four csv files (XXXXX are numbers):
 - XXXXX_agent_info.csv: information for the used hardware APU/GPU and CPU
 - XXXXX_kernel_traces.csv: information per each call of the kernel
 - XXXXX_kernel_stats.csv: statistics grouped by each kernel
 - XXXXX_domain_stats.csv: statistics grouped by domain, such as KERNEL_DISPATCH, HIP_COMPILER_API
- Content of results.stats.csv to see the list of GPU kernels with their durations and percentage of total GPU time:

"Name", "Calls", "TotalDurationNs", "AverageNs", "Percentage", "MinNs", "MaxNs", "StdDev" "NormKernell", 1001, 365858158, 365492.665335, 53.49, 360561, 449240, 3460.551681 "JacobiIterationKernel", 1000, 171479968, 171479.968000, 25.07, 162040, 205241, 10113.842491 "LocalLaplacianKernel", 1000, 135771713, 135771.713000, 19.85, 130400, 145121, 3349.580100 "HaloLaplacianKernel", 1000, 7777189, 7777.189000, 1.14, 7000, 12120, 349.399610 "NormKernel2", 1001, 3107927, 3104.822178, 0.4544, 2200, 138681, 6466.048652 "__amd_rocclr_fillBufferAligned", 1, 2720, 2720.0000000, 3.977e-04, 2720, 2720, 0.0000000e+00

In a spreadsheet viewer, it is easier to read:

	А	В	С	D	E	F	G	Н
1	Name	Calls	TotalDurationNs	AverageNs	Percentage	MinNs	MaxNs	StdDev
2	NormKernel1	1001	365858158	365492.665	53.49	360561	449240	3460.552
3	JacobilterationKernel	1000	171479968	171479.968	25.07	162040	205241	10113.84
4	LocalLaplacianKernel	1000	135771713	135771.713	19.85	130400	145121	3349.58
5	HaloLaplacianKernel	1000	7777189	7777.189	1.14	7000	12120	349.3996
6	NormKernel2	1001	3107927	3104.82218	0.4544	2200	138681	6466.049
7	amd_rocclr_fillBufferAligned	1	2720	2720	3.98E-04	2720	2720	0

[Public]

rocprofv3: Collecting Application Traces

 rocprof can collect a variety of trace event types, and generate timelines in JSON format for use with Perfetto, currently, however better use the pftrace output format (--output-format pftrace):

Trace Event	rocprof Trace Mode
HIP API call	hip-trace
GPU Kernels	kernel-trace
Host <-> Device Memory copies	hip-trace ormemory-copy-trace
CPU HSA Calls	hsa-trace
User code markers	marker-trace
Collect HIP, HSA, Kernels, Memory Copy, Marker API	sys-trace
Scratch memory operations	scratch-memory-trace

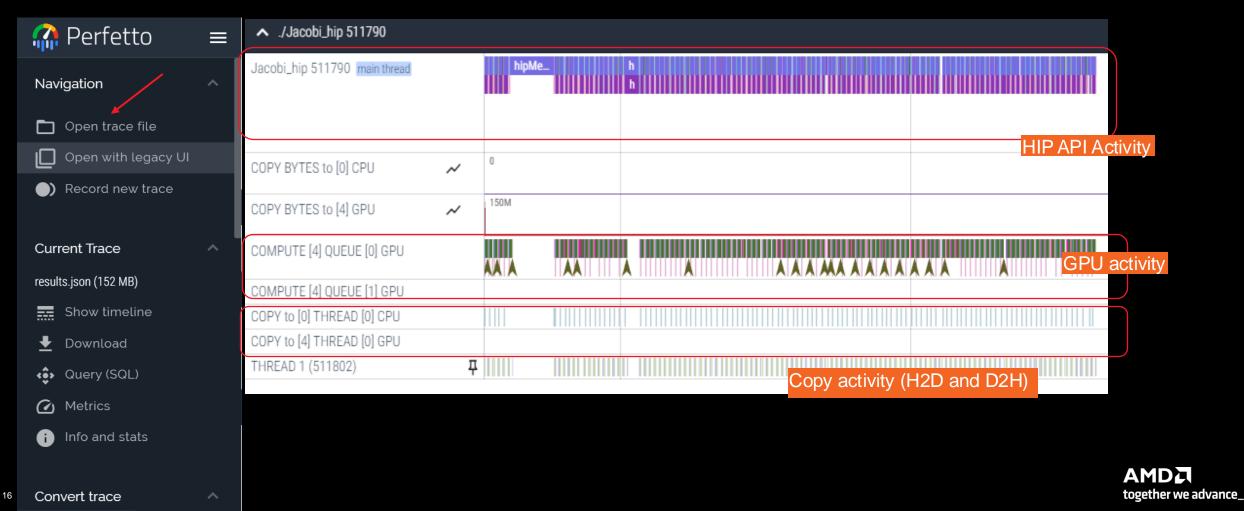
• You can combine modes like --stats --hip-trace --hsa-trace --output-format pftrace

[Public]

rocprof + Perfetto: Collecting and Visualizing Application Traces

- rocprof can collect traces
 - \$ /opt/rocm/bin/rocprof --hip-trace --output-format pftrace -- <app with arguments>

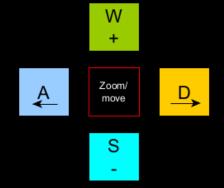
This will output a pftrace file that can be visualized using the chrome browser and Perfetto (https://ui.perfetto.dev/)



Perfetto: Visualizing Application Traces

- Zoom in to see individual events
- Navigate trace using WASD keys

▲ Misc Global Tracks	
Clock Snapshots	
 ./Jacobi_hip 511790 	
Jacobi_hip 511790 main thread	hipEven hipLaunc hipLaunchKern hipLaun hipLaun h h h hsa hsasig h h hsa
COPY BYTES to [0] CPU 🖌	
COPY BYTES to [4] GPU 🖌	150M
COMPUTE [4] QUEUE [0] GPU	LocalLaplacianKernel(int, int, int, double, double, double const*, double*) [clone .kd]
COMPUTE [4] QUEUE [1] GPU	
COPY to [0] THREAD [0] CPU	D
COPY to [4] THREAD [0] GPU	
THREAD 1 (511802)	



Perfetto: Kernel Information and Flow Events

- Zoom and select a kernel, you can see the link to the HIP call launching the kernel
- Try to open the information for the kernel (button at bottom right)

▲ Misc Global Tracks													
Clock Snapshots	꾸												
∧ ./Jacobi_hip 511790													
Jacobi_hip 511790 main thread		h hipLaun	chKernel hsa	hipEven	hipLa hs	hipLau hs	hipLa h	hipLa h		ISA_S	hipMemc hsa_s	a_signal_wait_sca.	
COPY BYTES to [0] CPU	\sim	0											
COPY BYTES to [4] GPU	\sim	150M											
COMPUTE [4] QUEUE [0] GPU			l		LocalLa	placianKern	el(int, int, ir	nt, <mark>double</mark> , (double, doubl	e const*, doul	ole*) [clone .kd]	HaloL	
COMPUTE [4] QUEUE [1] GPU													÷.
Current Selection												\uparrow	· ~



Perfetto: Kernel Information

:	Current Selectio	n				<u>↑</u> ×
Slic	Ce LocalLaplacia	anKernel(int, int, int, double, double, double const*, double*) [clone .kd]	Kernel name and a	rgs	Contextual Optio	ns 🕶
	Name	LocalLaplacianKernel(int, int, int, double, double, double const*, double*) [clone .kd]	Slice	Delay	Thread	
1	Category	kernel_dispatch	hsa_signal_store_screlease 🗡	<u>4us 110ns</u>	Jacobi_hip 511790 (./Jacobi_hip 511790)	
(Start time	00:00:00.969713738				
1	Absolute Time	2024-10-01T10:53:58.837832382	Arguments			
ſ	Duration	138us 520ns	\sim debug			
1	Process	./Jacobi_hip [511790]	begin_ns -	45564	433481727591	
1	SQL ID	slice[4481] -	end_ns -	45564	433481866111	
			delta_ns -	13852	20	
			kind -	11		
			agent -	4		
			corr_id -	4364		
			queue -	4		
			tid -	51179	90	
			kernel_id -	13		
			private_segment_size -	0		
			group_segment_size -	0	Workgroup	o size
			workgroup_size ~	256	grid size	
			grid_size -	16777	7216	
			legacy_event.passthrough_ut	i d - 1		_



Rocprofv3: OpenMP Offloading

- The option --kernel-trace provides information of the OpenMP kernels, good to use --hsa-trace if you want information from HSA layer
- For example:

srun -n 1 rocprofv3 --stats --kernel-trace --output-format pftrace -- <app with arguments>

Content of XXXXX_kernel_stats.csv:

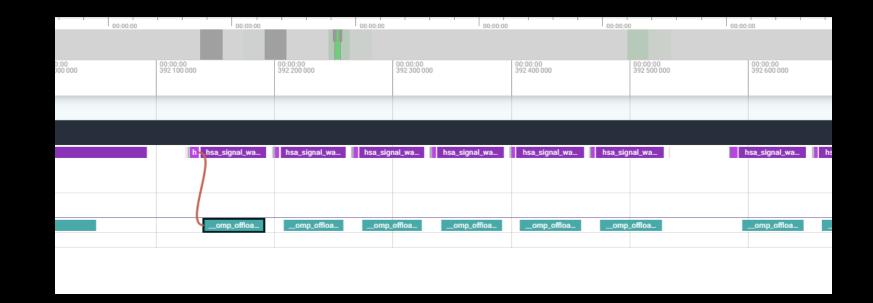
"Name", "Calls", "TotalDurationNs", "AverageNs", "Percentage", "MinNs", "MaxNs", "StdDev"

___omp_offloading_32_7f7a__Z6evolveR5FieldS0_dd_l24",500,45818062,91636.124000,100.00,49840,19483408,868965.767084

Content of XXXXX_kernel_trace.csv

"Kind","Agent_Id","Queue_Id","Kernel_Id","Kernel_Name","Correlation_Id","Start_Timestamp","End_Timestamp","Private_Segment_Size","Group_Segment_Size"," Workgroup_Size_X","Workgroup_Size_Y","Workgroup_Size_Z","Grid_Size_X","Grid_Size_Y","Grid_Size_Z" "KERNEL_DISPATCH",4,1,1,"__omp_offloading_32_7f7a__Z6evolveR5FieldS0_dd_I24",1,4547852833814530,4547852853297938,0,0,256,1,1,233472,1,1 "KERNEL_DISPATCH",4,1,1,"__omp_offloading_32_7f7a__Z6evolveR5FieldS0_dd_I24",2,4547852853393869,4547852853446789,0,0,256,1,1,233472,1,1 "KERNEL_DISPATCH",4,1,1,"__omp_offloading_32_7f7a__Z6evolveR5FieldS0_dd_I24",2,4547852853393869,4547852853446789,0,0,256,1,1,233472,1,1 "KERNEL_DISPATCH",4,1,1,"__omp_offloading_32_7f7a__Z6evolveR5FieldS0_dd_I24",3,4547852853461519,4547852853514599,0,0,256,1,1,233472,1,1

Perfetto and OpenMP visualization

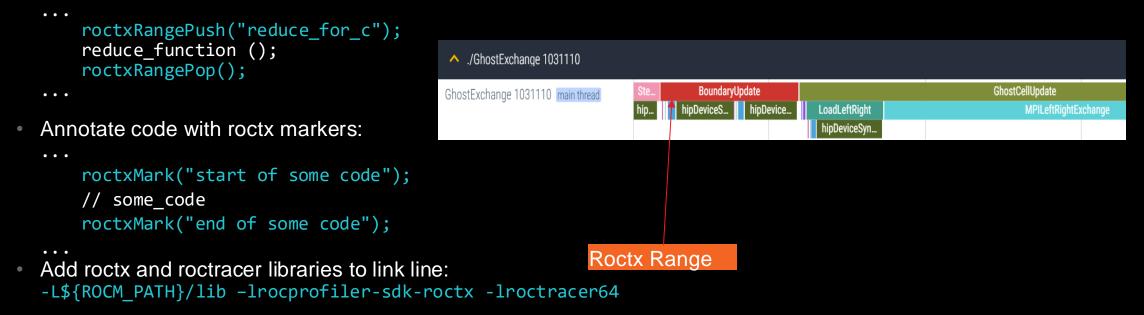


Using: --sys-trace --output-format pftrace We can use: --kernel-trace --output-format pftrace

 end_ns -	4552720951004323
delta_ns -	50880
kind -	11
agent -	4
corr_id ~	631
queue -	1
tid -	503089
kernel_id -	1
private_segment_size -	0
group_segment_size -	0
workgroup_size -	256
grid_size -	233472
legacy event passtbrough utid -	1

rocprofv3: Collecting Application Traces with rocTX Markers and Regions

- rocprofv3 can collect user defined regions or markers using rocTX
- Annotate code with roctx regions: #include <rocprofiler-sdk-roctx/roctx.h>



Profile with --roctx-range option:

\$ /opt/rocm/bin/rocprofv3 --hip-trace --marker-trace -- <app with arguments>

• Important: There is some difference regarding roctx between rocprof and rocprofv3

[Public]

Rocprofv3: Merge traces

- When you have one pftrace per MPI processes you can merge them as follows:
 - For example cat XXXXX_results.pftrace > all_ghostexchange.pftrace
 - Then visualize the file called all_ghostexchange.pftrace

▲ ./GhostExchange 1175256					
GhostExchange 1175256 main thread	hipDeviceSynchronize	h hipDeviceSynchr	hipDeviceSynchronize hipDeviceSynchronize		hipDeviceSynchro
COMPUTE [4] QUEUE [0] GPU	blur(double**, double**, int, int) [clone .kd]	enfo	enf gho		ghos
 ./GhostExchange 1175258 					
GhostExchange 1175258 main thread	hipDeviceSynchronize		hip hipDeviceSynchronize h hipDev	iceSynchronize h hipDeviceSynchronize	hi hipDeviceSynchronize
COMPUTE [7] QUEUE [0] GPU	blur(double**, double**, int, int) [clone .kd]		enfo_	enf gho	ghos
 ./GhostExchange 1175257 					
GhostExchange 1175257 main thread	hipDeviceSynchronize	ĥ	hipDeviceSynchronize hi hipDe	viceSynchronize h hipDeviceSynchronize	h hipDeviceSynchronize
COMPUTE [5] QUEUE [0] GPU	blur(double**, double**, int, int) [clone .kd]		enfo_	enf ghos	ghos
 ./GhostExchange 1175259 					
GhostExchange 1175259 main thread	hipDeviceSynchronize	h	hipDeviceSyn hi hipDeviceSynchronize	h hipDeviceSynchronize	hi hipDeviceSynchronize
COMPUTE [6] QUEUE [0] GPU	blur(double**, double**, int, int) [clone .kd]		enfo_ enf_	ghos	ghos

rocprofv3: Commonly Used GPU Counters

VALUUtilization	The percentage of ALUs active in a wave. Low VALUUtilization is likely due to high divergence or a poorly sized grid
VALUBusy	The percentage of GPUTime vector ALU instructions are processed. Can be thought of as something like compute utilization
FetchSize	The total kilobytes fetched from global memory
WriteSize	The total kilobytes written to global memory
MemUnitStalled	The percentage of GPUTime the memory unit is stalled
MemUnitStalled CU_OCCUPANCY	The percentage of GPUTime the memory unit is stalled The ratio of active waves on a CU to the maximum number of active waves supported by the CU
	The ratio of active waves on a CU to the maximum number of

rocprofv3: Collecting Hardware Counters

- rocprofv3 can collect a number of hardware counters and derived counters
 - \$ /opt/rocm/bin/rocprofv3 -L
- Specify counters in a counter file. For example:
 - \$ /opt/rocm/bin/rocprofv3 -i rocprof_counters.txt -- <app with args>
 - \$ cat rocprof_counters.txt
 pmc: VALUUtilization VALUBusy FetchSize WriteSize MemUnitStalled
 pmc: GPU_UTIL CU_OCCUPANCY MeanOccupancyPerCU MeanOccupancyPerActiveCU
 - A limited number of counters can be collected during a specific pass of code
 - Each line in the counter file will be collected in one pass
 - You will receive an error suggesting alternative counter ordering if you have too many / conflicting counters on one line
 - One directory per pmc line will be created, for example pmc_1 and pmc_2 for the two lines in the file with the counters.
 - One agent_info and one counter_collection csv file per MPI process will be created containing all the requested counters for each invocation of every kernel

[Public]

- As with every profiling tool, there is an overhead
- The percentage of the overhead depends on the profiling options used
 For example, tracing is faster than hardware counter collection
- When collecting many counters, the collection may require multiple passes
- With rocTX markers/regions, tracing can take longer and the output may be large
 - Sometimes too large to visualize
- The more data collected, the more the overhead of profiling
 - Depends on the application and options used
- rocprofv3 has less overhead than rocprof (v1) on various examples with extensive ROCm calls

Summary

- rocprofv3 is the open source, command line AMD GPU profiling tool distributed with ROCm 6.2 and later
- rocprofv3 provides tracing of GPU kernels, through various options, HIP API, HSA API, Copy activity and others
- rocprofv3 can be used to collect GPU hardware counters with additional overhead
- Perfetto seems to visualize pftrace files without significant issues
- Other output files are in text/CSV format

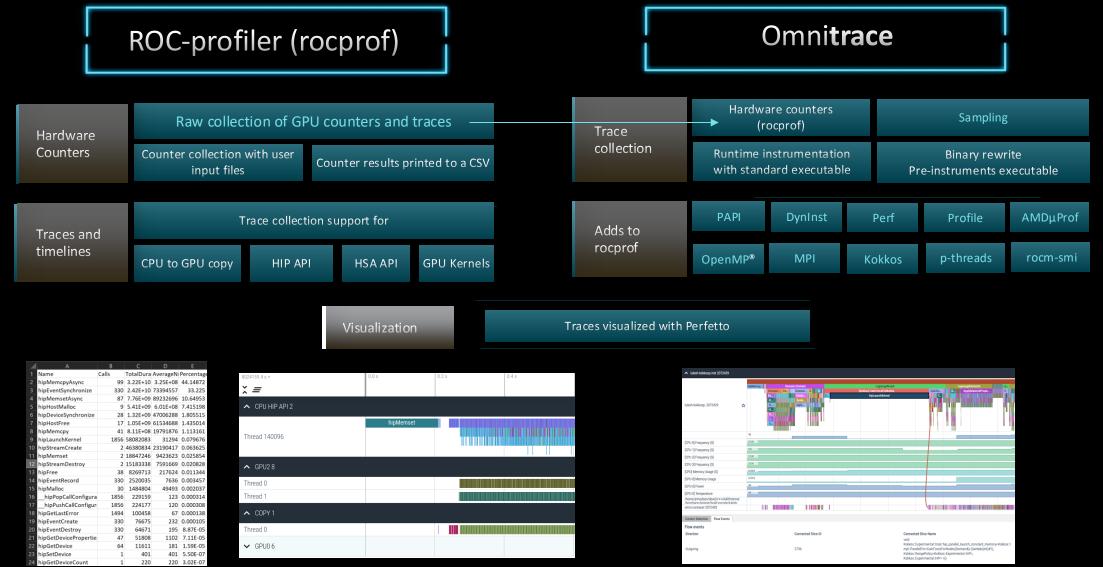


System Profiling with Omnitrace

Presenter: Sam Antao LUMI Pre-hackathon training Oct 8th, 2024

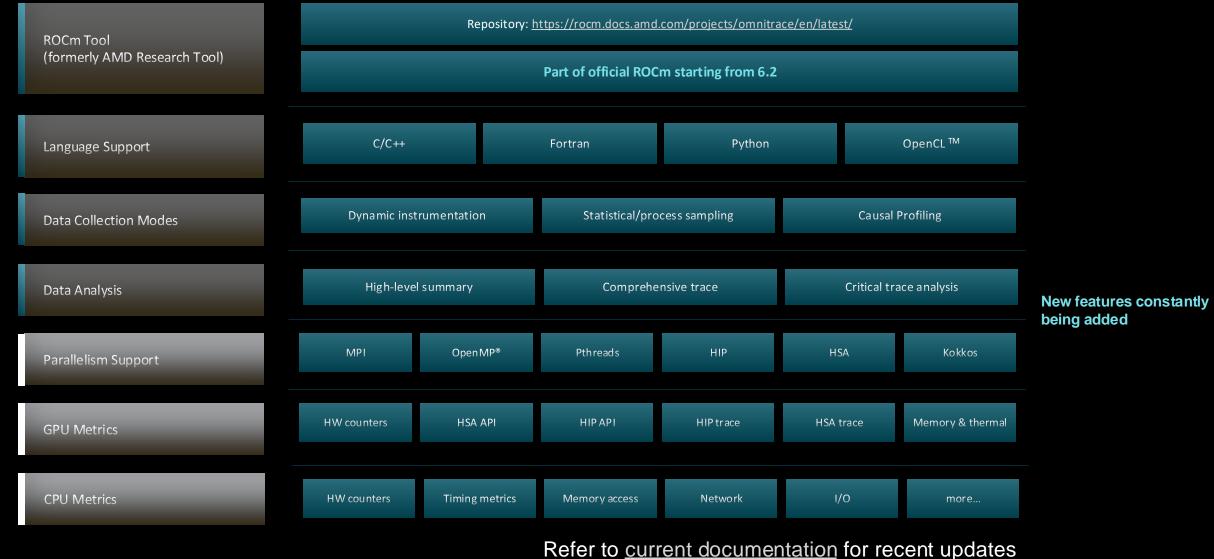
> AMD together we advance_

AMD Profilers with Timeline Profiling Support



AMD together we advance_

Omnitrace: Application Profiling, Tracing, and Analysis



30

AMD @ EPCC

Omnitrace Configuration Options

\$ omnitrace-avail --categories [options]

Get more information about run-time settings, data collection capabilities, and available hardware counters. For more information or help use -h/--help flags:

\$ omnitrace-avail -h

Collect information for Omnitrace-related settings using shorthand -c for --categories:

\$ omnitrace-avail -c rocm

		۸۱
ENVIRONMENT VARIABLE	VALUE	CATEGORIES
OMNITRACE_ROCM_EVENTS OMNITRACE_SAMPLING_GPUS OMNITRACE_USE_RCCLP OMNITRACE_USE_ROCM_SMI OMNITRACE_USE_ROCPROFILER OMNITRACE_USE_ROCTRACER OMNITRACE_USE_ROCTX	 0 false true true true true	<pre>custom, hardware_counters, libomnitrace, omnitrace, rocm, rocprofiler custom, libomnitrace, omnitrace, process_sampling, rocm, rocm_smi backend, custom, libomnitrace, omnitrace, rocl, rocm backend, custom, libomnitrace, omnitrace, process_sampling, rocm, rocm_smi backend, custom, libomnitrace, omnitrace, rocm, rocprofiler backend, custom, libomnitrace, omnitrace, rocm, roctracer backend, custom, libomnitrace, omnitrace, rocm, roctracer</pre>

Shows all runtime settings that may be tuned for rocm

Omnitrace Configuration File

\$ omnitrace-avail --categories [options]

Get more information about run-time settings, data collection capabilities, and available hardware counters. For more information or help use -h/--help flags:

\$ omnitrace-avail -h

Collect information for omnitrace-related settings using shorthand -c for --categories:

\$ omnitrace-avail -c omnitrace

For brief description, use the options:

\$ omnitrace-avail -bd

ENVIRONMENT VARIABLE	DESCRIPTION
OMNITRACE CAUSAL BINARY EXCLUDE OMNITRACE CAUSAL BINARY SCOPE OMNITRACE CAUSAL DELAY OMNITRACE CAUSAL DURATION OMNITRACE CAUSAL FUNCTION EXCLUDE	Excludes binaries matching the list of provided regexes from causal experiments (separated by tab, sem Limits causal experiments to the binaries matching the provided list of regular expressions (separated Length of time to wait (in seconds) before starting the first causal experiment Length of time to perform causal experimentation (in seconds) after the first experiment has started Excludes functions matching the list of provided regexes from causal experiments (separated by tab, se
OMNITRACE_CAUSAL_FUNCTION_SCOPE OMNITRACE_CAUSAL_RANDOM_SEED OMNITRACE_CAUSAL_SOURCE_EXCLUDE	List of <function> regex entries for causal profiling (separated by tab, semi-colon, and/or quotes (si Seed for random number generator which selects speedups and experiments please note that the lines Excludes source files or source file + lineno pair (i.e. <file> or <file>:<line>) matching the list of </line></file></file></function>
OMNITRACE_CAUSAL_SOURCE_SCOPE OMNITRACE_CONFIG_FILE OMNITRACE_CRITICAL_TRACE	Limits causal experiments to the source files or source file + lineno pair (i.e. <file> or <file>:<lin <br="">Configuration file for omnitrace Enable generation of the critical trace </lin></file></file>
OMNITRACE_ENABLED OMNITRACE_OUTPUT_PATH	Activation state of timemory Explicitly specify the output folder for results
OMNITRACE_OUTPUT_PREFIX	Explicitly specify a prefix for all output files PAPI presets and events to collect (see also: papi avail)
OMNITRACE_PERFETTO_BACKEND	Specify the perfetto backend to activate. Options are: 'inprocess', 'system', or 'all'
OMNITRACE_PERFETTO_BUFFER_SIZE_KB OMNITRACE_PERFETTO_FILL_POLICY	Size of perfetto buffer (in KB) Behavior when perfetto buffer is full. 'discard' will ignore new entries, 'ring_buffer' will overwrite
OMNITRACE_PROCESS_SAMPLING_DURATION OMNITRACE_PROCESS_SAMPLING_FREQ	If > 0.0, time (in seconds) to sample before stopping. If less than zero, uses OMNITRACE_SAMPLING_DURA Number of measurements per second when OMNITTRACE_USE_PROCESS_SAMPLING=ON. If set to zero, uses OMNITR DOED before contacts and the second se
OMNITRACE_ROCM_EVENTS OMNITRACE_SAMPLING_CPUS OMNITRACE_SAMPLING_DELAY	ROCm hardware counters. Use ':device=N' syntax to specify collection on device number N, e.g. ':device CPUs to collect frequency information for. Values should be separated by commas and can be explicit or Time (in seconds) to wait before the first sampling signal is delivered, increasing this value can fix
OMNITRACE_SAMPLING_DELAY OMNITRACE_SAMPLING_DURATION OMNITRACE_SAMPLING_FRE0	If > 0.0, time (in seconds) to wait before the first sampling signal is derivered, increasing this value can fix If > 0.0, time (in seconds) to sample before stopping Number of software interrupts per second when OMNITTRACE USE SAMPLING=ON
OMNITRACE_SAMPLING_FREQ	Devices to query when OMNITRACE USE_ROCM_SMI=ON. Values should be separated by commas and can be expli

Create a config file

Create a config file in \$HOME:

\$ omnitrace-avail -G \$HOME/.omnitrace.cfg

To add description of all variables and settings, use:

\$ omnitrace-avail -G \$HOME/.omnitrace.cfg --all

Modify the config file \$HOME/.omnitrace.cfg as desired to enable and change settings:

<snip> OMNITRACE TRACE = true OMNITRACE PROFILE = true OMNITRACE USE SAMPLING = false OMNITRACE_USE_ROCTRACER = true OMNITRACE USE ROCM SMI = true OMNITRACE USE MPIP = true OMNITRACE USE PID = true OMNITRACE USE ROCPROFILER = true OMNITRACE_USE_ROCTX = true <snip>

Contents of the config file

Declare which config file to use by setting the environment:

\$ export OMNITRACE_CONFIG_FILE=/pathto/.omnitrace.cfg

Binary Rewrite

Binary Rewrite	<pre>[omnitrace][exe] [internal] parsing library: '/usr/lib64/libgcc_s-8-20210514.so.1' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libnss_compat-2.28.so' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libnss_files-2.28.so' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libpthread-2.28.so'</pre>
<pre>\$ omnitrace-instrument [omnitrace-options] -o <new-name- of-exec> <cmd> <args></args></cmd></new-name- </pre>	<pre>[omnitrace][exe] [internal] parsing library: '/usr/lib64/libresolv-2.28.so' [omnitrace][exe] [internal] parsing library: '/usr/lib64/librt-2.28.so' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libstdc++.so.6.0.25' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libthread_db-1.0.so' [omnitrace][exe] [internal] parsing library: '/usr/lib64/libutil-2.28.so'</pre>
Generating a new executable/library with instrumentation built-in:	[omnitrace][exe] [internal] parsing library: '/usr/lib64/libz.so.1.2.11' [omnitrace][exe] [internal] binary info processing required 0.666 sec and 110.500 MB [omnitrace][exe] Processing 9 modules [omnitrace][exe] Processing 9 modules Done (0.001 sec, 0.000 MB)
<pre>\$ omnitrace-instrument -o Jacobi_hip.inst/Jacobi_hip</pre>	<pre>[omnitrace][exe] Found 'MPI_Init' in '/home/ssitaram/git/HPCTrainingExamples/HIP/jacobi/Jacobi_hip'. Enabling MPI support [omnitrace][exe] Finding instrumentation functions [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/available.json' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/available.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/available.txt' Done</pre>
This new binary will have instrumented functions	<pre>[omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/instrumented.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/excluded.json' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/excluded.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/excluded.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/excluded.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/overlapping.txt' Done [omnitrace][exe] Outputting 'omnitrace-Jacobi_hip.inst-output/2023-03-15_12.57/instrumentation/overlapping.txt' Done [omnitrace][exe] [omnitrace][exe] The instrumented executable image is stored in '/home/ssitaram/git/HPCTrainingExamples/HIP/jacobi/Jacobi_hip.inst' [omnitrace][exe] Getting linked libraries for /home/ssitaram/git/HPCTrainingExamples/HIP/jacobi/Jacobi_hip</pre>
	[omnitrace][exe] [omnitrace][exe] [omnitrace][exe] [omnitrace][exe] [omnitrace][exe]
Subroutine Instrumentation	[omnitrace][exe]/lib64/libpthread.so.0[omnitrace][exe]/lib64/libm.so.6[omnitrace][exe]/lib64/librt.so.1
Default instrumentation is main function and functions of 1024 instructions and more (for CPU)	[omnitrace][exe] /home/ssitaram/cp2k-hip/libs/install/openmpi/lib/libmpi.so.40 [omnitrace][exe] /opt/rocm-5.4.3//lib/libroctracer64.so.4 [omnitrace][exe] /opt/rocm-5.4.3/lib/libroctracer64.so.4 [omnitrace][exe] /opt/rocm-5.4.3/hip/lib/libmpi.so.40 [omnitrace][exe] /opt/rocm-5.4.3/hip/lib/libmpi.so.40 [omnitrace][exe] /opt/rocm-5.4.3/hip/lib/libmpi.so.40 [omnitrace][exe] /opt/rocm-5.4.3/hip/lib/libmpi.so.40 [omnitrace][exe] /lib64/libstdc++.so.6 [omnitrace][exe] /lib64/libstdc++.so.6
To instrument routines with 500 or more cycles, add option "-i 500" (more overhead)	[omnitrace][exe] /lib64/ld-linux-x86-64.so.2

Run Instrumented Binary

Binary Rewrite

\$ omnitrace-instrument [omnitrace-options] -o <new-nameof-exec> -- <CMD> <ARGS>

Generating a new executable/library with instrumentation built-in:

\$ omnitrace-instrument -o Jacobi_hip.inst -- ./Jacobi_hip

Run the instrumented binary:

\$ mpirun -np 1 omnitrace-run -- ./Jacobi_hip.inst -g 1 1

Subroutine Instrumentation

Default instrumentation is main function and functions of 1024 instructions and more (for CPU)

To instrument routines with 500 or more cycles, add option "-i 500" (more overhead)

Binary rewrite is recommended for runs with multiple ranks as Omnitrace produces separate output files for each rank

omnitrace][3624331][omnitrace init tooling] Instrumentation mode: Trace



omnitrace v1.8.0

953.765] perfetto.cc:58656 Configured tracing session 1, #sources:1, duration:0 ms, #buffers:1, total buffer si e:1024000 KB, total sessions:1, uid:0 session name: "" opology size: 1 x 1 _ocal domain size (current node): 4096 x 4096 omnitrace][0][pid=3624331] MPI rank: 0 (0), MPI size: 1 (1) Global domain size (all nodes): 4096 x 4096 Rank 0 selecting device 0 on host TheraC60 Starting Jacobi run. Iteration: 0 - Residual: 0.022108 teration: 100 - Residual: 0.000625 [teration: 200 - Residual: 0.000371 teration: 300 - Residual: 0.000274 teration: 400 - Residual: 0.000221 teration: 500 - Residual: 0.000187 Generates traces for application run teration: 600 - Residual: 0.000163 teration: 700 - Residual: 0.000145 teration: 800 - Residual: 0.000131 teration: 900 - Residual: 0.000120 teration: 1000 - Residual: 0.000111 Stopped after 1000 iterations with residue 0.000111 otal Jacobi run time: 1.5470 sec. Measured lattice updates: 10.84 GLU/s (total), 10.84 GLU/s (per process) Measured FLOPS: 184.36 GFLOPS (total), 184.36 GFLOPS (per process) Measured device bandwidth: 1.04 TB/s (total), 1.04 TB/s (per process) omnitrace][3624331][0][omnitrace finalize] finalizing... omnitrace][3624331][0][omnitrace finalize] omnitrace][3624331][0][omnitrace finalize] omnitrace/process/3624331 : 2.364423 sec wall clock, 645.964 MB peak rss, 388.739 MB page_rss, 4.330000 sec cpu_clock, 183.1 % cpu_util [laps: 1] omnitrace][3624331][0][omnitrace finalize] omnitrace/process/3624331/thread/0 : 2.355893 sec wall clock, 1.293230 sec thread cpu clock, 54.9 % thread cpu util, 645.964 MB peak rss [laps: 1] omnitrace][3624331][0][omnitrace finalize] omnitrace/process/3624331/thread/1 : 2.345084 sec wall clock, 0.000261 sec thread cpu clock, 0.0 % thread cpu util, 642.676 MB peak rss [laps: 1] omnitrace][3624331][0][omnitrace finalize]

omnitrace][3624331][0][omnitrace finalize] Finalizing perfetto...

Kernel Durations

\$ cat omnitrace-Jacobi_hip.inst-output/2024-01-01_13.57/wall_clock-0.txt

If you do not see a wall_clock.txt dumped by Omnitrace, try modify the config file \$HOME/.omnitrace.cfg and enable OMNITRACE_PROFILE (or prepend to your mpirun command):

OMNITRACE_PROFILE

= true

											— Dur	ations
0>>>	MPI Allreduce	1	5	wall clock	500	0 000012	0 000012	0.000012	0.000012	0 00000	0.000000	100.0
0>>>	hipDeviceSynchronize	1		wall clock				•	0.000012			94.4
0>>>	NormKernell(int, double, double, double const*, double*)	1		wall clock				•	0.000001			100.0
0>>>	NormKernel2(int, double, double, double*)	1		wall clock					0.000000			100.0
0>>>	_NVIMarrier	1		wall clock				•	0.000001			100.0
0>>>	_in_Louritecord			wall clock					0.000016			100.0
0>>>	Halo D2H::Halo Exchange			wall clock					1.628420			0.0
0>>>	_hipStreamSynchronize Call Stack	i i		wall clock					0.000003			100.0
0>>>	MPI Exchange :: Halo Exchange	i i		wall clock					1.628395			0.0
0>>>	MPI Waitall	i i		wall clock					0.000002			100.0
0>>>	Halo H2D::Halo Exchange	i i		wall clock				•	1.628104			0.0
0>>>	hipStreamSynchronize	i īi		wall clock					0.000003			100.0
0>>>	hipLaunchKernel	5		wall clock					0.000578			99.6 j
0>>>	mbind	i 1i		wall clock				•	0.000003			100.0
0>>>	hipMemcpy	1		wall clock				0.001122	•			99.9
0>>>		1		wall clock			0.000000	•	0.000000		0.000000	100.0
0>>>		1		wall clock				•	0.000000			100.0
0>>>	_JacobiIterationKernel(int, double, double, double const*, double const*, double*, double*)	1		wall_clock					0.000000		0.000000	100.0

Kernel Durations – Flat Profile

Edit in your omnitrace.cfg (or prepend to your mpiru	un command):
OMNITRACE_PROFILE	= true
OMNITRACE_FLAT_PROFILE	= true

Use flat profile to see aggregate duration of kernels and functions

REAL-CLOCK TIMER (I.E. WALL-CLOCK TIMER)											
LABEL	COUNT	DEPTH	METRIC	UNITS	SUM	MEAN	MIN	MAX	VAR	STDDEV	% SELF
0>>> main	1	0	wall clock	1	82.739099	82.739099	82.739099	82.739099	0.000000	1	100.0
0>>> MPI Init	1	0	wall_clock	sec	34.056610	34.056610	34.056610	34.056610	0.000000	0.000000	100.0
0>>> pthread create	3	Θ	wall_clock	sec	0.014644	0.004881	0.001169	0.011974	0.000038	0.006145	100.0
0>>> mbind	285	Θ	wall_clock	sec	0.001793	0.000006	0.000005	0.000020	0.000000	0.000002	100.0
0>>> MPI_Comm_dup	1	Θ	wall_clock	sec	0.000212	0.000212	0.000212	0.000212	0.000000	0.000000	100.0
0>>> MPI_Comm_rank	1	Θ	wall_clock	sec	0.000041	0.000041	0.000041	0.000041	0.000000	0.00000	100.0
0>>> MPI_Comm_size	1	Θ	wall_clock	sec	0.000004	0.000004	0.000004	0.000004	0.000000	0.00000	100.0
0>>> hipInit	1 /	Θ	wall_clock	sec	0.000372	0.000372	0.000372	0.000372	0.000000	0.00000	100.0
0>>> hipGetDeviceCount	1 /	Θ	wall_clock	sec	0.000017	0.000017	0.000017	0.000017	0.000000	0.00000	100.0
0>>> MPI_Allgather	1	Θ	wall_clock	sec	0.00009	0.00009	0.000009	0.00009	0.000000	0.000000	100.0
0>>> hipSetDevice	1	0	wall_clock	sec	0.000024	0.000024	0.000024	0.000024	0.000000		100.0
0>>> hipHostMalloc	3	0	wall_clock	sec	0.126827	0.042276	0.000176	0.126453	0.005314		100.0
0>>> hipMalloc	7	0	wall_clock	sec	0.000458	0.000065	0.000024	0.000178	0.000000	0.000052	100.0
0>>> hipMemset	1	0	wall_clock	sec	35.770403	35.770403	35.770403	35.770403	0.000000	0.000000	100.0
0>>> hipStreamCreate	2	0	wall_clock	sec	0.016750	0.008375	0.005339	0.011412	0.000018		100.0
0>>> hipMemcpy	1005	0	wall_clock	sec	8.506781	0.008464	0.000610	0.039390	0.000023	0.004844	100.0
0>>> hipEventCreate	2	0	wall_clock	sec	0.000037	0.000018	0.000016	0.000021	0.000000	0.000003	100.0
0>>> hipLaunchKernel	5002	0	wall_clock	sec	0.181301	0.000036	0.000025	0.012046	0.000000	0.000278	100.0
0>>> MPI_Allreduce	1003	0	wall_clock wall clock	sec	0.002009	0.000002	0.000001	0.000022	0.000000	0.000001	100.0
0>>> hipDeviceSynchronize 0>>> MPI Barrier	1001 3	0	wall_clock	sec	0.016813	0.000017	0.000015	0.000043	0.000000	0.000004	100.0 100.0
0>>> hipEventRecord	2000	0	wall_clock	sec sec	0.046701	0.000023	0.000020	0.000225	0.000000	0.000006	
0>>> hipStreamSynchronize	2000	0	wall_clock	sec sec	0.030366	0.000015	0.000013	0.000382	0.000000	0.000009	
0>>> MPI Waitall	1000	0	wall clock	sec sec	0.001665	0.000002	0.000002	0.000007	0.000000	0.000000	100.0
0>>> NormKernel1(int, double, double, double const*, double*)	1000	0	wall_clock	sec	0.001502	0.000002	0.000001	0.000006	0.000000	0.000000	100.0
0>>> NormKernel2(int, double, double, double const*, double*)	1001	0	wall_clock	sec	0.001972	0.000002	0.000001	0.000003	0.000000	0.000001	100.0
0>>> LocalLaplacianKernel(int, int, int, double, double, double const*, double*)	1000	0	wall_clock	sec	0.001488	0.000001	0.000001	0.000007	0.000000	0.000000	100.0
0>>> HaloLaplacianKernel(int, int, int, double, double, double const*, double const*, double*)	1000	0	wall clock	sec	0.001465	0.000001	0.000001	0.000007	0.000000	0.000000	100.0
10>>> hipEventElapsedTime	1000	0	wall clock	sec	0.015060	0.000015	0.000014	0.000041	0.000000	0.000002	100.0
<pre> ->>> hipsterict(b); acobilterationKernel(int, double, double, double const*, double const*, double*, double*)</pre>	1000	0	wall clock	sec	0.002598	0.000003	0.000001	0.000006	0.000000	0.000001	100.0
0>>> pthread join	1000	0	wall clock	sec	0.000396	0.000396	0.000396	0.000396	0.000000	0.000000	100.0
lo>> hipFree	4	0	wall clock	sec	0.000526	0.000131	0.000021	0.000243	0.000000	0.000091	100.0
0>>> hipHostFree	2	0	wall clock	sec	0.000637	0.000318	0.000287	0.000350	0.000000	0.000044	100.0
3>>> start thread	ī	0	wall clock	sec	0.004802	0.004802	0.004802	0.004802	0.000000	0.000000	100.0
1>>> start_thread	ī	0	wall clock	sec	81.987779	81.987779	81.987779	81.987779	0.000000		100.0
2>>> start_thread		0		-	-	-	-	-	-	-	-

Visualizing Trace (1/3)

Use Perfetto

Copy perfetto-trace-0.proto to your laptop, go to <u>https://ui.perfetto.dev/</u>, click "Open trace file", select perfetto-trace-0.proto

4676921.1 s +		0.0 s	0.2 s	0.4 s	0.6 s	0.8 s	1.0 s	1.2 s	1.4 s	1.6 s	1.8 s	2.0 s	2.2 s
× =													
Clock Snapshots metric					Å								A
 ./Jacobi_hip.inst 3624331 													
	(main											
Jacobi_hip.inst 3624331		MPI_Ini	it	Jacobi_t::Jacobi_t CreateMesh::Init hipMemset									
CPU Context Switches (S)	\sim	25 K							Tra	ces of CP	U functio	ns	
CPU Frequency [0] (S)	\sim	5 K											
CPU Frequency [1] (S)	\sim	2.5 K											
CPU Frequency [2] (S)	\sim	2.5 K											
CPU Frequency [3] (S)	\sim	2.5 K											
CPU Frequency [4] (S)	\sim	2.5 K											
CPU Frequency [5] (S)	\sim	2.5 K											
CPU Frequency [6] (S)	\sim	2.5 K											
CPU Frequency [7] (S)	\sim	2.5 K											
CPU Frequency [8] (S)	CPU	metrics											
CPU Frequency [9] (S)	\sim	2.5 K											
CPU Frequency [10] (S)	\sim	2.5 K											

Visualizing Trace (2/3)

2.5 K

 \sim

Use Perfetto

CPU Frequency [10] (S)

Zoom in to investigate regions of interest



./Jacobi_hip.inst 3624331 main Halo D2H: Halo H2D::... hipDe... MPI_All ipEven.. hipEventRecord nipLaunchK. hipEve... . hipLaunc. hipLa. hipMemcpy Jacobi_hip.inst 3624331 nipS.. MPI_Waita... hipSt. Zoomed in CPU Context Switches (S) 25 K \sim CPU Frequency [0] (S) \sim 5 K 2.5 K CPU Frequency [1] (S) \sim 2.5 K CPU Frequency [2] (S) \sim 2.5 K CPU Frequency [3] (S) \sim CPU Frequency [4] (S) \sim 2.5 K 2.5 K CPU Frequency [5] (S) \sim 2.5 K CPU Frequency [6] (S) \sim 2.5 K CPU Frequency [7] (S) \sim 2.5 K CPU Frequency [8] (S) \sim 2.5 K CPU Frequency [9] (S) \sim

Visualizing Trace (3/3)

Use Perfetto





4676921.1 s +			0.75236 s	0.75238 s	0.75240 s	0.75242 s	0.75244 s	0.75246 s	0.75248 s	0.75250 s	0.75252 s	0.75254 s	0.75256 s	0.75258 s	0.75260 s
× =															
./Jacobi_hip.inst 3624331	*														
HIP Activity Device 8, Queue 0	*					Flow Ev	ents								Marker
HIP Activity Device 8, Queue 1	*						(LocalL	aplacianKernel(int,	int, int, double, do	ouble, double const	*, double*)		
		MPI_All	hipDe	hinEven	ninEventRecord	hint aunchK	hinEve	Halo D2H:	main MPI Exchange::Hal	Halo H2D::	hinl aune	hint au hint au	hint a	hinMe	meny
Jacobi_hip.inst 3624331			MPI_All hipDex hipEvent hipEventRecord hipEvent hipEvent												
		clos	se toget	her											
CPU Context Switches (S)	∕★	25 K													
CPU Frequency [0] (S)	∕★	5 K													
CPU Kernel Time (S)	∕★	5													
CPU Memory Usage (S)	∕★	0.75 K													
CPU Page Faults (S)	∕★	50 K													
CPU Peak Memory (S)	∕★	0.75 K													
CPU User Time (S)	∕★	2.5													
CPU Virtual Memory Usage (S)	/★	50 K													
GPU Busy [0] (S)	/*	100													
GPU Memory Usage [0] (S) GPU (cha	ract	orietice												
GPU Temperature [0] (S)	JIA ∕★	50													
	1														

Hardware Counters – List All

true

| perf::CYCLES + exclusive access

\$ omnitrace-avail --all

Components, Categories

1 × 1	1			1		
COMPONENT	AVAILABLE	VALUE_TYPE	STRING_IDS	FILENAME	DESCRIPTION	CATEGORY
allinea_map caliper_marker caliper_config caliper_loop_marker	false false false false	void void void void	"allinea", "allinea_map", "forge" "cali", "caliper", "caliper_marker" "caliper_config" "caliper_loop_marker" "cpu clock"		Caliper configuration manager. Variant of caliper_marker with support fo	<pre>category::external, os::supports_linux, t category::external, os::supports_unix, tp category::external, os::supports_unix, tp category::external, os::supports_unix, tp</pre>
cpu_clock cpu util	true true	std::pair <long, long=""></long,>	"cpu util", "cpu utilization"			<pre>project::timemory, category::timing, os:: project::timemory, category::timing, os:: </pre>
craypat counters	false	<pre>std::vector<unsigned long,="" pre="" std::allocato<=""></unsigned></pre>	"craypat counters"	craypat counters		category::external, os::supports linux, t

[
ENVIRONMENT VARIABLE	VALUE	DATA TYPE	DESCRIPTION	CATEGORIES	
OMNITRACE_CAUSAL_BINARY_EXCLUDE OMNITRACE CAUSAL BINARY SCOPE	%MAIN%			analysis, causal, custom, libomnitrace, o analysis, causal, custom, libomnitrace, o	
OMNITRACE_CAUSAL_DELAY	0	double	Length of time to wait (in seconds) befor	analysis, causal, custom, libomnitrace, o	
OMNITRACE_CAUSAL_DURATION OMNITRACE CAUSAL FUNCTION EXCLUDE	0		Length of time to perform causal experime Excludes functions matching the list of p	analysis, causal, custom, libomnitrace, o analysis, causal, custom, libomnitrace, o	variables
OMNITRACE_CAUSAL_FUNCTION_EXCLODE		string	List of <function> regex entries for caus</function>	analysis, causal, custom, libomnitrace, o	
OMNITRACE_CAUSAL_RANDOM_SEED	Θ		Seed for random number generator which se		
OMNITRACE_CAUSAL_SOURCE_EXCLUDE			Excludes source files or source file + li		
OMNITRACE_CAUSAL_SOURCE_SCOPE		string	Limits causal experiments to the source f	analysis, causal, custom, libomnitrace, o	

			TCC NORMAL WRITEBACK sum:device=0	l true	Number of writebacks due to requests that
HARDWARE COUNTER	AVAILABLE	DESCRIPTION	TCC_ALL_TC_OP_WB_WRITEBACK_sum:device=0	l true	Number of writebacks due to requests that
	AVAILADLL	DESCRIPTION			
			TCC_NORMAL_EVICT_sum:device=0	true	Number of evictions due to requests that
CPU			<pre> TCC_ALL_TC_OP_INV_EVICT_sum:device=0</pre>	true	Number of evictions due to all TC_OP inva
			<pre> TCC_EA_RDREQ_DRAM_sum:device=0</pre>	true	Number of TCC/EA read requests (either 32
PAPI L1 DCM	true	Level 1 data cache misses	<pre> TCC_EA_WRREQ_DRAM_sum:device=0</pre>	true	Number of TCC/EA write requests (either 3
PAPI_L1_ICM	false	Level 1 instruction cache misses	FETCH SIZE:device=0	true	The total kilobytes fetched from the vide
PAPI L2 DCM	true	Level 2 data cache misses	WRITE SIZE:device=0	true	The total kilobytes written to the video
PAPI L2 ICM	true	Level 2 instruction cache misses	WRITE REQ 32B:device=0	j true	The total number of 32-byte effective mem
PAPI L3 DCM	false	Level 3 data cache misses	GPUBusy:device=0	true	The percentage of time GPU was busy.
		· · · · · · · · · · · · · · · · · · ·	Wavefronts:device=0 GPU Hardware	Countara	
PAPI_L3_ICM	false	Level 3 instruction cache misses	VALUInsts:device=0	Counters	The average number of vector ALU instruct
PAPI_L1_TCM		Level 1 cache misses	SALUInsts:device=0	true	The average number of scalar ALU instruct
CPU Hardware Cou	nters		SFetchInsts:device=0	true	The average number of scalar fetch instru
perf::CYCLES	true	PERF COUNT HW CPU CYCLES	GDSInsts:device=0	l true	The average number of GDS read or GDS wri
perf::CYCLES:u=0	true	perf::CYCLES + monitor at user level		1	The percentage of GPUTime the memory unit
perf::CYCLES:k=0	true	perf::CYCLES + monitor at kernel level	MemUnitBusy:device=0	true	
perf::CYCLES:h=0		perf::CYCLES + monitor at hypervisor level	ALUStalledByLDS:device=0	true	The percentage of GPUTime ALU units are s
	true				
perf::CYCLES:period=0	true	perf::CYCLES + sampling period			
perf::CYCLES:freq=0	true	<pre>perf::CYCLES + sampling frequency (Hz) </pre>			
perf::CYCLES:precise=0	true	perf::CYCLES + precise event sampling	A very small subset of the	counters s	shown here
nonf. (VCLEC. ovol-0	+	parts CVCLEC + avaluative pagage			

AMD together we advance_

perf::CYCLES:excl=0

Configure Omnitrace to Collect GPU Hardware Counters

Modify config file Modify the config file \$HOME/.omnitrace.cfg to add desired metrics and for concerned GPU#ID: OMNITRACE ROCM EVENTS = FetchSize:device=0, VALUUtilization:device=0, MemUnitBusy:device=0 To profile desired metrics for all participating GPUs: OMNITRACE ROCM EVENTS = FetchSize, VALUUtilization, MemUnitBusy Note: currently experiencing issues with ROCm 6.2.1, fix coming soon

Full list of GPU metrics at https://github.com/ROCm/rocprofiler/blob/amd-staging/test/tool/metrics.xml

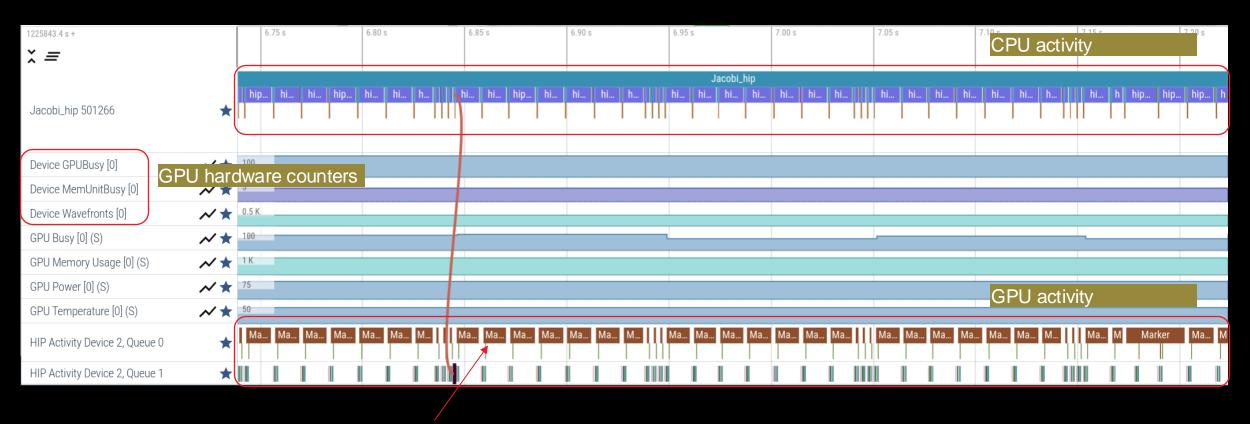
Execution with Hardware Counters

After modifying .cfg file to set up OMNITRACE_ROCM_EVENTS with GPU metrics run: \$ mpirun -np 1 omnitrace-run -- ./Jacobi_hip.inst -g 1 1

[omnitrace][1056814][0][omnitrace finalize] omnitrace][1056814][0][omnitrace finalize] Finalizing perfetto... omnitrace][1056814][perfetto]> Outputting '/datasets/teams/dcqpu training/lstanisi/test hackmd3/HPCTrainingExamples/HIP/jacobi/omnitrace-Jacobi hip.inst-outpu t/2024-10-02 10.36/perfetto-trace-0.proto' (8130.87 KB / 8.13 MB / 0.01 GB)... Done [omnitrace][1056814][rocprof-device-0-FetchSize]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02_10.36/rocprof-device-0-FetchSize-0.json' omnitrace][1056814][rocprof-device-0-FetchSize]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/rocprof-device-0-FetchSize-0.txt' omnitrace][1056814][rocprof-device-0-VALUUtilization]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/rocprof-device-0-VALUUtilization-0.json omnitrace][1056814][rocprof-device-0-VALUUtilization]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/rocprof-device-0-VALUUtilization-0.txt' omnitrace][1056814][rocprof-device-0-MemUnitBusy]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/rocprof-device-0-MemUnitBusy-0.json' omnitrace][1056814][rocprof-device-0-MemUnitBusy]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10,36/rocprof-device-0-MemUnitBusy-0.txt' omnitrace][1056814][wall clock]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/wall clock-0.json [omnitrace][1056814][wall_clock]> Outputting 'omnitrace-Jacobi_hip.inst-output/2024-10-02_10.36/wall_clock-0.txt' omnitrace][1056814][roctracer]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/roctracer-0.json' omnitrace][1056814][roctracer]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/roctracer-0.txt' omnitrace][1056814][metadata]> Outputting 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/metadata-0.json' and 'omnitrace-Jacobi hip.inst-output/2024-10-02 10.36/functions-0.json'

GPU hardware counters

Visualization with Hardware Counters



ROCTX Regions



Sampling CPU Call-Stack (1/2)

OMNITRACE_USE_SAMPLING = true; OMNITRACE_SAMPLING_FREQ = 100 (100 samples per second) Alternatively run with omnitrace-sample

	samples [omnitrace]	
Jacobi_t::Jacobi_t(grid_t&, mesh_t&)]]]]]]]]]]]]]]]]]]]]
Jacobi_t::CreateMesh()	J NNN h NNLNN NLNN h NJHLHN	1 N N N N N N L N N N N N N N <mark>h h h</mark> N L N N N N N N N N N H N N N
hipMemset	h h h h h h h h h h h h h h s h h h h h	s h h h h h h h h h h h h h h h <mark>h s s h h</mark> h h h h h h h h h h h h h h h h
hipApiName	h h sh O h h sh h h sh h O h s sh h h s	s h h h h h <mark>s h</mark> h h h h h h h <mark>h 0 0 h s</mark> h h h h h h h h h h h h h h
hipDeviceGetByPCIBusId	h h <mark>0 h h h 0 h h h 0 h h 0 h h 0 h 0 h</mark>	h h h h h h h h h h h h h h <mark>h s 0 h</mark> 0 h h h h h h h <mark>h h h 0 0 h</mark> h
hipExtStreamGetCUMask	h h <mark>s</mark> h hh <mark>0</mark> hh h <mark>0</mark> hh h <mark>0</mark> hh	h h h h h h h h h h h h <mark>h h s _</mark> h <mark>s</mark> h h h h h h h h h h h
hipExtStreamGetCUMask	h hsh hh <mark>0</mark> hh h <mark>0</mark> hh h <mark>_0</mark> h	hhhhhh <mark>h</mark> hhhhhh <mark>hhs</mark> hhhhhhhhhhh 0 hs
hiprtcLinkAddData	h h <mark>s</mark> h hh hh h <mark>s</mark> hh h <mark>h</mark>	hhhhhh hhhhhhhhhh <mark>0</mark> hh <mark>s</mark> h
hiprtcLinkAddData	h h h h h h <mark>s</mark> h h h h	hhhhhh hhhhhhh <mark>r</mark> hh <mark>k</mark> h
hiprtcLinkAddData	h h h h h h h h h h	hhhhhh hhhhhh h
hiprtcLinkAddData	h h h h h h h h h h	h h h h h h h h h h h h h h h h h h h
hiprtcLinkAddData	h h h h h h h h h h	hhhhhh hhhhh <mark>r</mark> hhhhhhhhhh
hiprtcLinkAddData	h r r r r r r r r r	T T T T T T T T T T T T T T T T T T T
hiprtcLinkAddData	h h h h h h h h h h h	h h h h h h h h h h h h h h h h h h h
hiprtcLinkAddData	r	
hiprtcLinkAddData	h	
hiprtcLinkAddData	h	
amd_comgr_do_action	h	Each sample shows the
amd_comgr_data_set_remove	h	
amd_comgr_data_set_remove		call stack at that time
amd_comgr_data_set_remove		
amd_comgr_data_set_remove		
amd_comgr_data_set_remove		

Scroll down all the way in Perfetto to see the sampling output

Sampling CPU Call-Stack (2/2)

Zoom in call-stack sampling

					samples [omnitrace	e]				
Jacobi	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Run()	Jacobi_t::Ru
Norm(gr	LocalLaplacian(gri	Norm(grid_t&, me	Norm(grid_t&, me	hipEventRecord	Norm(grid_t&, me	Jacobilteration(HaloExchange(gri	LocalLaplacian(g	HaloExchange(grid	Norm(grid_t&
hipMemc	hipLaunchKernel	hipMemcpy	hipMemcpy	std::basic_string<	hipMemcpy	hipLaunchKernel	hipStreamSynchro	hipLaunchKernel	hipStreamSynchroni	hipMemcpy
hipApiN	std::basic_string<	hipApiName	hipApiName	OnUnload	hipApiName	std::basic_strin	std::basic_strin	hipMemPoolGetAtt	hipLaunchHostFunc	hipApiName
hiprtcL	OnUnload	hiprtcLinkAddData	hiprtcLinkAddData	OnUnload	hiprtcLinkAddData	OnUnload	OnUnload	hip_impl::hipLau	OnUnload	hiprtcLinkAd
hiprtcL	OnUnload	hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData		OnUnload	hipGetCmdName	OnUnload	hiprtcLinkAd
hiprtcL	OnUnload	hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData			hipGetPCH	OnUnload	hiprtcLinkAd
hiprtcL	std::ostream& std:	hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData			hipIpcGetEventHa		hiprtcLinkAd
hiprtcL	std::ostreambuf_it	hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData					hiprtcLinkAd
hiprtcL		hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData					hiprtcLinkAd
hiprtcL		hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData					hiprtcLinkAd
hiprtcL		hiprtcLinkAddData	hiprtcLinkAddData		hiprtcLinkAddData					hiprtcLinkAd
roctrac		roctracer_disabl	roctracer_disabl		roctracer_disabl					roctracer_di
hsa_amd		hsa_amd_image_ge	hsa_amd_image_ge		hsa_amd_image_ge					hsa_amd_imag

Thread 0 (S) 3625610

Sampling data is annotated with (S)



Additional Features

- Dynamic runtime instrumentation
- User API to control instrumentation
- OMNITRACE_USE_KOKKOSP=true supports Kokkos profiling
- omnitrace-python supports Python[™] profiling (only with AMD Research ROCm)
- omnitrace-causal for invoking causal profiling (experimental)

Fixes coming soon:

- Hardware counters
- Full OpenMP[®] support
- Visualizing traces from multiple MPI ranks

Summary

- Omnitrace powerful tool to understand CPU + GPU activity on AMD GPUs
 - Ideal for an initial look at how an application runs
 - Easy to visualize traces in Perfetto
- Leverages several other tools and combines their data into a comprehensive output files
 - Some tools used are AMDµProf, rocprofiler, rocm-smi, roctracer, perf, etc.
- Helps users analyze overlaps between CPU/GPU compute and communication

Other profiling options

Presenter: Sam Antao LUMI Pre-hackathon training October 8th, 2024



TAU

- Tuning and Analysis Utilities, developed at University of Oregon
- Scalable and flexible performance analysis toolkit
- Automatic instrumentation through Program Database Toolkit (PDT) for routines, loops, I/O, memory, phases, etc.

HPCToolkit: Overview

- Not an AMD profiler developed for 20+ years, mainly at Rice University
- HPCToolkit suite of tools for tracing, profiling and analyzing parallel programs
- Combine sampling data with a static analysis of the program structure for loops and inline functions
- Present top-down, bottom-up and flat views of calling context tree and time-sequence trace view
- Low overhead, easy to use, interactive analysis with hpcviewer
- No instrumentation or recompilation needed, as long as "-g" compiler flag is used
- Supports threads (pthreads, OpenMP[®]), MPI, hybrid (MPI+threads), and GPUs (AMD, Intel[®], NVIDIA)

Hands-on Exercises

https://hackmd.io/@sfantao/lumi-prehack-oct-2024

We encourage you to look at our HPC Training Examples repo for other examples: <u>https://github.com/amd/HPCTrainingExamples</u>

A table of contents for the READMEs if available at the top-level <u>README</u> in the repo

Rocprofv3 exercises instructions: <u>Rocprofv3/README.md</u> Link to instructions on how to run Omnitrace tests: <u>Omnitrace/omnitrace_jacobi/MI200/README.md</u>

Questions?

ssh <you user>@lumi.csc.fi

https://hackmd.io/@sfantao/lumi-prehack-oct-2024

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