Profiler Tools Overview

Gina Sitaraman, Suyash Tandon, Justin Chang, Julio Maia, Noel Chalmers, Paul T. Bauman, Nicholas Curtis, Nicholas Malaya, Alessandro Fanfarillo, Jose Noudohouenou, Chip Freitag, Damon McDougall, Noah Wolfe, Jakub Kurzak, <u>Samuel Antao</u>, George Markomanolis, Bob Robey, Essam Morsi

LUMI Performance Tunning Workshop Jun 11-12th, 2024

AMD together we advance_

Background – AMD Profilers

ROC-profiler (rocprof)					Omni trace				ך ר	Omni perf					
Hardware	Raw collection of GPU counters and traces				Trace	Comprehensive trace collection				Performance	Automated collection of hardware counters				
Counters	Counter collection with Counte user input files			sults printed I CSV	collection	c	PU	GPU		Analysis		Analysis		Visualization	
Traces and timelines	Trace collection support for CPU copy HIP API HSA API GPU Kerr		or GPU Kernels	Supports	CPU copy OpenMP®	HIP API MPI Ko	HSA API GPU Ke okkos p-threads multi	ernels i-GPU	Supports	Speed of Light	Memory chart	Rooflines	Kernel comparison		
Visualisation	lisation Traces visualized with Perfetto			Visualisation	Visualisation Traces visualized with Perfetto				Visualisation With Grafana or standalone GUI						
	A 1 Name 2 hip/emcpyAsync 3 hip/emcpyAsync 4 hip/MemcpyAsync 5 hip/ost/Synchronize 4 hip/Memcgy 9 hip/Launch/Kernel 10 hip/Stree 7 hip/Memcpy 9 hip/Launch/Kernel 10 hip/StreamCreate 11 hip/Memset 12 hip/StreamDestroy 13 hip/Fee 14 hip/StreamDestroy 13 hip/Fee 14 hip/StreamDestroy 15 hip/DallConfigur 16 hip/OpCallConfigur 19 hip/StreatCreate 20 hip/StreatCreate 21 hip/StreatCreate 22 hip/StreatCreate 23 hip/StreatCreate 24 hip/StreatCreate 25 hip/StreatCreate 26 hip/StreatCreate 27 hip/StreatCreate 28 hip/StreatCreate 29 hip/StreatCreate 20 hip/StreatCreate 20 hip/StreatCreate 20 hip/StreatCreate 20 hip/StreatCreate 20 hip/StreatCreate 21 hip/StreatCreate 23 hip/StreatCreate 24 hip/StreatCreate 25 hip/StreatCreate 26 hip/StreatCreate 27 hip/StreatCreate 28 hip/StreatCreate 29 hip/StreatCreate 20	B C Calls TotalDura Ave 99 3.22.F10 7.3 302 2.22.F10 7.3 87 7.76E+09 892 9 5.41E+09 6.0 17 1.05E+09 612 18 1.1E+08 197 125 58082083 . 2 46380834 21 2 15183388 75 38 8269713 2 30 1484804 . 310 1484804 . 310 1484804 . 330 66671 . 330 66675 . 330 66671 .	D E ageN: Percentage 44.14872 ageN: Percentage 44.14872 9457 3.3.25 8266 10.64953 16408 7.415198 8268 1.835515 9468 1.835515 94478 0.03625 94478 0.03625 94479 0.03625 94479 0.03625 9456 0.020854 9468 0.020854 9469 0.020828 7524 0.003457 94939 0.002037 123 0.000314 120 0.000308 670 0.00338 322 0.000155 1959 8.876-05		▲ Matchildekoop Intt 2017/20 Matchildekoop Intt 2017/20 Matchildekoop Intt 2017/20 Matchildekoop 2017/20 Port (Frequency (Laranghawi Janang Carlon Arwing Ng Anno Manul Ng Anno Ng Anno		Instr Buff Www 0 km or Www 6 k file Wave Occu 29 pp Wave Life 3405 cp	Instr Dispatch		Image: Second	L2 Cache	C SCMI/ PCIe ↓ Fabric Verses 0 20 pto 0 20 pto	
	21 hipGetDevicePropertie22 hipGetDevice23 hipSetDevice24 hipGetDeviceCount	e: 47 51808 64 11611 1 401 1 220	1102 7.11E-05 181 1.59E-05 401 5.50E-07 220 3.02E-07		Direction	Connected Slice ID	Comm veid Kokku mp1:3 Kokku Kokku	stetd Slice Name 4: Experimental.impl:hip.garallet_launch_constant_memory-Kokkos:1 tranle@ior-Kohkodes(Domanik):[amhda[nt]#1], 5: Experimental.HIP>, 4: Experimental.HIP>, +()							

Background – AMD Profilers



Background – AMD Profilers



What is ROC-Profiler?

• ROC-profiler (also referred to as rocprof) is the command line front-end for AMD's GPU profiling libraries

- Repo: <u>https://github.com/ROCm-Developer-Tools/rocprofiler</u>
- rocprof contains the central components allowing application traces and counter collection
 - Under constant development
- Distributed with ROCm
- The output of rocprof can be visualized in the Chrome browser with Perfetto (<u>https://ui.perfetto.dev/</u>)
- There are ROCProfiler V1 and V2 (roctracer and rocprofiler into single library, same API)
- A new rocprofiler-sdk is going to be released soon, the repository is public: <u>https://github.com/ROCm/rocprofiler-sdk</u> development is **still** going on, no version is released yet

rocprof: Getting Started + Useful Flags

 To get help: \${ROCM PATH}/bin/rocprof -h

- Useful housekeeping flags:
 - --timestamp <on|off> turn on/off gpu kernel timestamps
 - --basenames <on|off> turn on/off truncating gpu kernel names (i.e., removing template parameters and argument types)
 - -o <output csv file> Direct counter information to a particular file name
 - -d <data directory> Send profiling data to a particular directory
 - -t <temporary directory> Change the directory where data files typically created in /tmp are placed. This allows you to save these temporary files.
- Flags directing rocprofiler activity:
 - -i input<.txt|.xml> specify an input file (note the output files will now be named input.*)
 - --hsa-trace to trace GPU Kernels, host HSA events (more later) and HIP memory copies.
 - --hip-trace to trace HIP API calls
 - --roctx-trace to trace roctx markers
 - --kfd-trace to trace GPU driver calls
- Advanced usage
 - -m <metric file> Allows the user to define and collect custom metrics. See <u>rocprofiler/test/tool/*.xml</u> on GitHub for examples.

[Public]

rocprof: Kernel Information

- rocprof can collect kernel(s) execution stats
 - \$ /opt/rocm/bin/rocprof --stats --basenames on <app with arguments>
- This will output two csv files:
 - results.csv: information per each call of the kernel
 - results.stats.csv: statistics grouped by each kernel
- Content of results.stats.csv to see the list of GPU kernels with their durations and percentage of total GPU time:

"Name","Calls","TotalDurationNs","AverageNs","Percentage"
"JacobiIterationKernel",1000,556699359,556699,43.291753895270446
"NormKernel1",1001,430797387,430367,33.500980655394606
"LocalLaplacianKernel",1000,280014065,280014,21.775307970480817
"HaloLaplacianKernel",1000,14635177,14635,1.1381052818810995
"NormKernel2",1001,3770718,3766,0.2932300765671734
"__amd_rocclr_fillBufferAligned.kd",1,8000,8000,0.0006221204058583505

In a spreadsheet viewer, it is easier to read:

	A	В	С	D	E
1	Name	Calls	TotalDurationNs	AverageNs	Percentage
2	JacobiIterationKernel	1000	556699359	556699	43.2917538952704
3	NormKernel1	1001	430797387	430367	33.5009806553946
4	LocalLaplacianKernel	1000	280014065	280014	21.7753079704808
5	HaloLaplacianKernel	1000	14635177	14635	1.1381052818811
6	NormKernel2	1001	3770718	3766	0.293230076567173
7	amd rocclr fillBufferAligned	1	8000	8000	0.000622120405858

rocprof + Perfetto: Collecting and Visualizing Application Traces

rocprof can collect traces

\$ /opt/rocm/bin/rocprof --hip-trace <app with arguments>

This will output a .json file that can be visualized using the Chrome browser and Perfetto (https://ui.perfetto.dev/)



AMD together we advance_

Omnitrace: Application Profiling, Tracing, and Analysis



Refer to current documentation for recent updates



Omnitrace instrumentation Modes



Basic command-line syntax:
<pre>\$ omnitrace [omnitrace-options] <cmd> <args></args></cmd></pre>
For more information or help use -h/help/? flags:
\$ omnitrace -h
Can also execute on systems using a job scheduler. For example, with SLURM, an interactive session can be used as:
<pre>\$ srun [options] omnitrace [omnitrace-options] <cmd> <args></args></cmd></pre>

For problems, create an issue here: <u>https://github.com/AMDResearch/omnitrace/issues</u> Documentation: <u>https://amdresearch.github.io/omnitrace/</u>

Omniperf: Automated Collection of Hardware Counters and Analysis



11

rocprof: Multiple MPI Ranks

- rocprof can collect counters and traces for multiple MPI ranks
- Say you want to profile an application usually called like this: mpiexec -np <n> ./Jacobi_hip -g <x> <y>
- Invoke the profiler by executing:

```
mpiexec -np <n> rocprof <rocprof_options> ./Jacobi_hip -g <x> <y>
or
srun --ntasks=n rocprof <rocprof options> ./Jacobi hip -g <x> <y>
```

- By directing output files from each rank to different directories, we can collect traces for each rank separately
 - Use a helper script for this, and run your program as shown below:
 mpiexec -np <n> helper_rocprof.sh ./Jacobi_hip -g <x> <y>
- Multi-node profiling currently isn't supported

Profiling Multiple MPI Ranks

\$cat rocprof_wrapper.sh

```
#!/bin/bash
set -euo pipefail
# depends on ROCM PATH being set outside; input arguments are the output directory & the name
outdir="$1"
name="$2"
if [[ -n ${OMPI_COMM_WORLD_RANK+z} ]]; then
    # mpich
    export MPI RANK=${OMPI COMM WORLD RANK}
elif [[ -n ${MV2 COMM WORLD RANK+z} ]]; then
    # ompi
    export MPI RANK=${MV2 COMM WORLD RANK}
elif [[ -n ${SLURM PROCID+z} ]]; then
    export MPI RANK=${SLURM PROCID}
else
    echo "Unknown MPI layer detected! Must use OpenMPI, MVAPICH, or SLURM"
    exit 1
fi
rocprof="${ROCM PATH}/bin/rocprof"
                                                              Output directory per rank
pid="$$"
                                                          Filenames annotated with rank as well
outdir="${outdir}/rank_${pid}_${MPI_RANK}"
outfile="${name}_${pid}_${MPI_RANK}.csv"
${rocprof} -d ${outdir} --hsa-trace -o ${outdir}/${outfile} ("${@:3}"
                                                                          Application and its arguments
```

Profiling Overhead

- As with every profiling tool, there is an overhead
- The percentage of the overhead depends on the profiling options used
 For example, tracing is faster than hardware counter collection
- When collecting many counters, the collection may require multiple passes
- With rocTX markers/regions, tracing can take longer and the output may be large
 - Sometimes too large to visualize
- The more data collected, the more the overhead of profiling
 - Depends on the application and options used

Disclaimer

The information presented in this document is for informational purposes only and may contain technical inaccuracies, omissions, and typographical errors. The information contained herein is subject to change and may be rendered inaccurate for many reasons, including but not limited to product and roadmap changes, component and motherboard version changes, new model and/or product releases, product differences between differing manufacturers, software changes, BIOS flashes, firmware upgrades, or the like. Any computer system has risks of security vulnerabilities that cannot be completely prevented or mitigated. AMD assumes no obligation to update or otherwise correct or revise this information. However, AMD reserves the right to revise this information and to make changes from time to time to the content hereof without obligation of AMD to notify any person of such revisions or changes.

THIS INFORMATION IS PROVIDED 'AS IS." AMD MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE CONTENTS HEREOF AND ASSUMES NO RESPONSIBILITY FOR ANY INACCURACIES, ERRORS, OR OMISSIONS THAT MAY APPEAR IN THIS INFORMATION. AMD SPECIFICALLY DISCLAIMS ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, OR FITNESS FOR ANY PARTICULAR PURPOSE. IN NO EVENT WILL AMD BE LIABLE TO ANY PERSON FOR ANY RELIANCE, DIRECT, INDIRECT, SPECIAL, OR OTHER CONSEQUENTIAL DAMAGES ARISING FROM THE USE OF ANY INFORMATION CONTAINED HEREIN, EVEN IF AMD IS EXPRESSLY ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

Third-party content is licensed to you directly by the third party that owns the content and is not licensed to you by AMD. ALL LINKED THIRD-PARTY CONTENT IS PROVIDED "AS IS" WITHOUT A WARRANTY OF ANY KIND. USE OF SUCH THIRD-PARTY CONTENT IS DONE AT YOUR SOLE DISCRETION AND UNDER NO CIRCUMSTANCES WILL AMD BE LIABLE TO YOU FOR ANY THIRD-PARTY CONTENT. YOU ASSUME ALL RISK AND ARE SOLELY RESPONSIBLE FOR ANY DAMAGES THAT MAY ARISE FROM YOUR USE OF THIRD-PARTY CONTENT.

© 2023 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, ROCm, and combinations thereof are trademarks of Advanced Micro Devices, Inc. in the United States and/or other jurisdictions. Other names are for informational purposes only and may be trademarks of their respective owners.

Linux is the registered trademark of Linus Torvalds in the U.S. and other countries.

The OpenMP name and the OpenMP logo are registered trademarks of the OpenMP Architecture Review Board

Python

Windows is a registered trademark of Microsoft Corporation in the US and/or other countries.

#