## Tools in action An example with Pytorch

Sam Antao

LUMI Advanced Training Mar. 7<sup>th</sup> 2025 AMD together we advance\_ slides on LUMI in /project/project\_465001726/Slides/AMD/

hands-on exercises: <u>https://hackmd.io/@sfantao/lumi-training-sto-2025#Pytorch-example</u> hands-on source code: /project/project\_465001726/Exercises/AMD/Pytorch

Agenda

**1. Intro to Pytorch and its dependencies** 

2. Controlling affinity

3. Profiling – rocprof and omnitools.

#### 4. Debugging

All suggestions presented here are transversal to any AI or HPC application!

Python applications can leverage the same tooling as non-python applications!

Scripting examples are suggestions and can always be adapted!

## Pytorch highlight

- Official page: <u>https://pytorch.org/</u>
- Code: https://github.com/pytorch/pytorch
- Python<sup>™</sup>-based framework for machine learning
  - Auto-differentiation on tensor types
- GPU-enabled
  - ROCm support for MI250x (and others)
  - Hipification as part of the build system
    - C/C++ libraries with proper bindings for Python
  - Python code does <u>NOT</u> need changing using the same CUDA conventions
- Other related packages:
  - Torch vision/audio, triton, many others
- Many more build on it
  - vLLM, Deepspeed, Megatron-LM





#### Pytorch install – our base environment

module purge module load CrayEnv module load PrgEnv-cray/8.5.0 module load craype-accel-amd-gfx90a module load cray-python



Setup the GPU environment and the Cray Python environment

> Recent Pytorch builds need recent user-level ROCm versions.

# This path provides more recent ROCm modules.
module use /appl/local/containers/test-modules

module load rocm/6.1.3.lua

We will be using 6.1.3 as it the oldest version for Pytorch 2.4.1 official releases

#### **Pytorch install – running the examples**

- For simplicity and improve the demonstration we leverage interactive runs on existing node allocation
- We run beforehand:



We are reserving <u>N nodes</u>, in this case only one node, using <u>one of the two</u> <u>available hardware threads</u> per core. We we'll be using the <u>8 GCDs</u> available in each node.

- This is a good way to experiment and converge to the correct job description.
- Don't forget to release your allocations once you are done!
- Once you consolidate your job description you can leverage batch jobs.
  - Salloc options translate directly to sbatch options.



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#### **Pytorch install – virtual environments**

Virtual environments are convenient to manage python package installation in ones user-space



leveraged by the install and run.

Install and run as before. No need to specify install location – the environment is doing it for you.

pip3 install --pre torch==2.4.1+rocm6.1 --index-url <u>https://download.pytorch.org/whl/</u> srun --jobid=\$jobid -n1 --gpus 8 \

python -c 'import torch; print("I have this many devices:", torch.cuda.device\_count())'

#### Pytorch install – conda environment



- Conda environment adds the package-manager functionality to a virtual environment
- One can tune the Python version to use as we won't be leveraging the system one anymore.
  - No module load cray-python needed!



pip3 install --pre torch==2.4.1+rocm6.1 --index-url https://download.pytorch.org/whl/ srun --jobid=\$jobid -n1 --gpus 8 \

python -c 'import torch; print("I have this many devices:", torch.cuda.device\_count())'

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#### Pytorch install – conda environment install from source

- Installing Pytorch from source is not recommended on LUMI
  - Too old default ROCm to build against.
- It might be useful in some cases: builds with symbols for debugging.

# We need a recent enough compiler – we'll use gcc module load PrgEnv-gnu/8.5.0 gcc/10.3.0

# Clone a given version of Pytorch and all its third\_party components

git clone -b v2.4.1 --recursive  $\setminus$ 

<u>https://github.com/pytorch/pytorch\_pytorch-source</u>

# Createand activate conda environment to manage the install conda create -y -n pytorch-from-source python=3.11 source \$wd/miniconda3/bin/activate pytorch-from-source

# Install requirements for Pytorch and some build tools. pip install -r \$wd/pytorch-source/requirements.txt

conda install -y cmake ninja

# Sometimes we need to solve some library clashes between conda and the system. We force the removal of the conda libstdc++ so that we use the system one.

rm -rf \$wd/miniconda3/envs/pytorch-from-source/lib/libstdc++.so

# Point to our ROCM instalation that is not in a default path.
grep -rl /opt/rocm | \
xargs sed -l "s#/opt/rocm#\$ROCM PATH#g"

# Hipify source nice python3 tools/amd\_build/build\_amd.py

# Build with debug symbols
CC=\$(which gcc)\
CXX=\$(which g++)\
CMAKE\_PREFIX\_PATH=\$CONDA\_PREFIX:\$CMAK\_PREFIX\_PATH \
LDFLAGS="-L\$ROCM\_PATH/deps -lstdc++ \_ltinfo"\
USE\_KINETO=0 BUILD\_TEST=0 \
PYTORCH\_ROCM\_ARCH=gfx90a \
REL\_WITH\_DEB\_INFO=1 \
nice python3 setup.py bdist\_wheel
build with

pip install \$wd/pytorch-source/dist/torch-\*.whl

Enable/disable

## Pytorch install – Singularity containers

- Control better the Pytorch environment
- Less strain on the filesystem
  - All application installation is loaded as a single file
- Enable more recent ROCm versions
- Transferable and arguably more portable
- Some containers available under:
  - /appl/local/containers/sif-images/



#### Any cons?

- Updating the environment and installing more packages may require rebuild the container
- Containers can't currently be build on LUMI:
  Needs containers to be built elsewhere and copied to the system
- Submitting jobs has to be done more carefully.



SIF=<myimage.sif>

![](_page_10_Picture_15.jpeg)

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## **Pytorch install – Singularity containers**

![](_page_11_Figure_2.jpeg)

![](_page_11_Picture_3.jpeg)

The container image to use: Pytorch 2.4.1 on top of ROCm 6.1.3

One could leverage a script to describe what is going to be executed inside the container.

> This script has to load the container Conda environment. A special variable is set in the container to facilitate that.

Run as before.

container and execute the script

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#### **Controlling device visibility**

- Controlling visibility
  - HIP\_VISIBLE\_DEVICES=0,1,2,3 python -c 'import torch; print(torch.cuda.device\_count())'
  - ROCR\_VISIBLE\_DEVICES=0,1,2,3 python -c 'import torch; print(torch.cuda.device\_count())'
  - SLURM sets ROCR\_VISIBLE\_DEVICES
  - Implications of both ways of setting visibility blit kernels and/or DMA
  - Considerations:
    - Does my app expects GPU visibility to be set in the environment?
    - Does my app expects arguments to define target GPUs
    - Does my app make any assumption on the device based on other information:
      - MPI rank
      - CPU-range
      - Auto-determined
    - How many processes using the same GPU:
    - Contention vs occupancy
    - Runtime scheduling limits
    - Increased scheduling complexity
    - Imbalance

Most Pytorch applications and driver scripts assume the GPU to be used corresponds to the local rank!!!

## **Testing affinity**

- What CPUs I have available and their NUMA domain?
  - Iscpu
- What GPUs I have
  - rocm-smi –showtopo

NUMA node0 CPU(s): NUMA node1 CPU(s): NUMA node2 CPU(s): NUMA node3 CPU(s):

![](_page_13_Figure_7.jpeg)

: (Topology) Numa Node: 3

- : (Topology) Numa Affinity: 3
- : (Topology) Numa Node: 3
- : (Topology) Numa Affinity: 3
- : (Topology) Numa Node: 1
- : (Topology) Numa Affinity: 1
- : (Topology) Numa Node: 1
- : (Topology) Numa Affinity: 1
- : (Topology) Numa Node: 0
- : (Topology) Numa Affinity: 0
- : (Topology) Numa Node: 0
- : (Topology) Numa Affinity: 0
- : (Topology) Numa Node: 2
- : (Topology) Numa Affinity: 2
- : (Topology) Numa Node: 2
- : (Topology) Numa Affinity: 2

ORNL topology - https://docs.olcf.ornl.gov/systems/crusher\_quick\_start\_guide.html

![](_page_14_Figure_2.jpeg)

![](_page_14_Picture_3.jpeg)

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## **Testing affinity**

#### Check what SLURM is giving us:

srun -c 7 -N 2 -n 16 --gpus 16 \

bash -c 'echo "\$SLURM\_PROCID -- GPUS \$ROCR\_VISIBLE\_DEVICES -- \$(taskset -p \$\$)"' \

| sort -n -k1

0 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54249's current affinity mask: fe 1 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54250's current affinity mask: fe00 2 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54251's current affinity mask: fe0000 3 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54252's current affinity mask: fe000000 4 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54253's current affinity mask: fe00000000 5 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54254's current affinity mask: fe0000000000 6 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54255's current affinity mask: fe000000000000 7 -- GPUS 0,1,2,3,4,5,6,7 -- pid 54256's current affinity mask: fe000000000000000 8 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110083's current affinity mask: fe 9 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110084's current affinity mask: fe00 10 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110085's current affinity mask: fe0000 11 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110086's current affinity mask: fe000000 12 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110087's current affinity mask: fe00000000 13 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110088's current affinity mask: fe0000000000 14 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110089's current affinity mask: fe000000000000 15 -- GPUS 0,1,2,3,4,5,6,7 -- pid 110090's current affinity mask: fe000000000000000

Careful! Allocations do not follow GPU ranking!!

Example 05

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## **Testing affinity**

• Check what SLURM is giving us:

srun -N 2 -n 16 --gpus 16 \

sort -n -k1

0	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	819's	current	affinity	mask:	fe000000000000
1	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	820's	current	affinity	mask:	fe000000000000000000000000000000000000
2	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	821's	current	affinity	mask:	fe0000
3	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	822 <b>'</b> s	current	affinity	mask:	fe000000
4	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	823's	current	affinity	mask:	fe
5	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	824's	current	affinity	mask:	fe00
6	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	825's	current	affinity	mask:	fe00000000
7	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	13	826's	current	affinity	mask:	fe0000000000
8	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	3 94	670 <b>'</b> s	current	affinity	mask:	fe000000000000
9	GPUS	0,1,	2,3,4	1,5,6	,7 -	- pic	3 94	671 <b>'</b> s	current	affinity	mask:	fe000000000000000000000000000000000000
10	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	ld 9	4672's	s current	t affinity	mask:	fe0000
11	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	ld 9	4673's	s current	t affinity	mask:	fe000000
12	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	ld 9	4674's	s current	t affinity	mask:	fe
13	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	ld 9	4675's	s current	t affinity	mask:	fe00
14	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	Ld 9	4676's	s current	affinity	mask:	fe00000000
15	- GPUS	5 0,1	,2,3,	4,5,	6,7	p:	Ld 9	4677's	s current	affinity	mask:	fe0000000000

#### Great! CPUs are properly bound to the GPUs!

![](_page_16_Picture_8.jpeg)

![](_page_16_Picture_9.jpeg)

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#### Pytorch example app – MNIST distributed learning

- Popular computer vision training dataset
- Al training iterate over epochs and a given sample batch is considered in each epoch
- Provided example runs over 3 epochs (properly trained models need much more than than!)
- MNIST training considers number images with different formats.

![](_page_17_Figure_5.jpeg)

![](_page_17_Picture_6.jpeg)

#### Pytorch example app – MNIST distributed learning

- What provides distributed capability:
  - Pytorch Distribute Data Parallel (DDP) Batches of different data run concurrently
  - Other more sophisticated methods available
  - Frameworks like Deepspeed and Horovod can also enable distributed training.

![](_page_18_Figure_5.jpeg)

- ... Epoch 0 Loss 0.148397 Global batch size 2048 on 16 ranks
- ... Epoch 1 Loss 0.147906 Global batch size 2048 on 16 ranks
- ... Epoch 2 Loss 0.147717 Global batch size 2048 on 16 ranks

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![](_page_18_Picture_11.jpeg)

Examples 06

#### Pytorch example app – MNIST distributed learning – RCCL

- RCCL should be set to use only high-speed-interfaces Slingshot
- The problem one might see on startup:

NCCL error in: /workdir/pytorchexample/pytorch/torch/csrc/distributed/c10d/ProcessGroupNCCL.cpp:1269, unhandled system error, NCCL version 2.12.12

• Check error origin by setting RCCL specific debug environment variables:

export NCCL DEBUG=INFO

Node has interfaces other than Slingshot

These are the correct ones.

NCCL INFO NET/Socket : Using [0]nmn0:10.120.116.65<0> [1]hsn0:10.253.6.67<0> [2]hsn1:10.253.6.68<0> [3]hsn2:10.253.2.12<0> [4]hsn3:10.253.2.11<0> NCCL INFO /long\_pathname\_so\_that\_rpms\_can\_package\_the\_debug\_info/data/driver/rccl/src/init.cc:1292

The fix:
 export NCCL SOCKET IFNAME=hsn0,hsn1,hsn2,hsn3

Point RCCL to use all 4 high-speed interfaces. It will know how to bind them based on the node topology.

#### Pytorch example app – MNIST distributed learning - script

<ul> <li>What can/should I include in my start script:</li> </ul>	Smoke test to confirm GPUs are available Stample 06								
if [ \\$SLURM_LOCALID -eq 0 ] ; then									
rocm-smi									
	lust-in-time compiles are a common technique in these								
export MIOPEN_USER_DB_PATH="/tmp/\$( <b>whoami</b> )-miopen-cache-\\$SLURM_NODEID" export MIOPEN_CUSTOM_CACHE_DIR=\\$MIOPEN_USER_DB_PATH	applications. MIOpen leverages this functionality. Let's cache								
# Report affinity echo "Rank \\$SLURM_PROCID> \\$(taskset -p \\$\\$)"	those builds in node-local storage instead of the default home folder. ROCm 6.2 may not need this.								
# Start conda environment inside the container \\$WITH_CONDA	Activate the container Conda environment that provides Pytorch								
# Set interfaces to be used by RCCL. export NCCL_SOCKET_IFNAME=hsn0,hsn1,hsn2,hsn3	Point RCCL to use the high-speed network interfaces								
# Set environment for the app									
export MASTER_ADDR=\\$(python /workdir/get-master.py "\\$SLURM_NODELIST")									
export MASTER_PORT=29500									
export RANK=\\$SLURM_PROCID	Iranslate SLURM environment into something that Pytorch DDP								
export ROCR_VISIBLE_DEVICES=\\$SLURM_LOCALID	understands								
# Run app									
cd /workdir/mnist									
python -u mnist_DDP.pygpumodelpath /workdir/mnist/model	Run my model training								

#### Pytorch example app – MNIST distributed learning - rocprof

- Rocprof profiler client is the easiest way to get started with GPU profiling.
- It is available as part of the ROCm stack and, therefore, available in the containers
- It is seldomly useful to profile every single process/rank of your app:
  - Profilling more than needed = more potential profiling overhead
  - Misleading conclusions

![](_page_21_Picture_6.jpeg)

Command to prepend to my application instantiation

if [ \$RANK -eq 2 ] ; then pcmd='rocprof --hip-trace'

pcmd="

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We want to profile only for one rank - in this case rank #2

Example

Run command as before except to the prepended profiling command

\$pcmd python -u mnist\_DDP.py --gpu --modelpath /workdir/mnist/model

#### More than one rank to be profiled? Use, –o myresults.\$RANK.csv, to make sure sure there are no races generating the profile files Mar 7th 2025 LUMI Advanced Training LUMI Advanced Training

## Pytorch example app – MNIST distributed learning - rocprof

Bound by RCCL communication!

	0 s	1 1 1	5 s	1 1 1	10 s	1 1	15 s	1 1 1	20 s	1 1 1	25 s	1 1	30 s	1 1	1	Example 0
274458.5 s +		30.16 s	30.18 s	30.20 s	30.22 s 3	0.24 s	30.26 s	30.28 s	30.30 s	30.32 s	30.34 s	30.36 s	30.38 s	30.40 s	30.4;	,
▲ CPU HIP API 2																Collectives
Thread 50305																kernels domina
∧ GPU6 12																promo
Thread 1	<barrier< td=""><td><barrier p<="" td=""><td>acket&gt;</td><td><barrier packet=""></barrier></td><td><barrier packet=""></barrier></td><td> barrie</td><td>r packet&gt;</td><td><ba< td=""><td>rrier packet&gt;</td><td></td><td><barrier packe<="" td=""><td>et&gt;</td><td><barrier packet=""></barrier></td><td>- sbarrie</td><td>er pac</td><td></td></barrier></td></ba<></td></barrier></td></barrier<>	<barrier p<="" td=""><td>acket&gt;</td><td><barrier packet=""></barrier></td><td><barrier packet=""></barrier></td><td> barrie</td><td>r packet&gt;</td><td><ba< td=""><td>rrier packet&gt;</td><td></td><td><barrier packe<="" td=""><td>et&gt;</td><td><barrier packet=""></barrier></td><td>- sbarrie</td><td>er pac</td><td></td></barrier></td></ba<></td></barrier>	acket>	<barrier packet=""></barrier>	<barrier packet=""></barrier>	 barrie	r packet>	<ba< td=""><td>rrier packet&gt;</td><td></td><td><barrier packe<="" td=""><td>et&gt;</td><td><barrier packet=""></barrier></td><td>- sbarrie</td><td>er pac</td><td></td></barrier></td></ba<>	rrier packet>		<barrier packe<="" td=""><td>et&gt;</td><td><barrier packet=""></barrier></td><td>- sbarrie</td><td>er pac</td><td></td></barrier>	et>	<barrier packet=""></barrier>	- sbarrie	er pac	
Thread 2	ncclKerne	ncclKernel_S	endRec no	clKernel_SendR	ncclKernel_SendR	ncclKern	el_SendRe	ncclKernel_SendR	ecv_RING_SIMPLE	_Sum	rernel_SendRecv_R	RING_SIMP	ncclKernel_SendRe	e ncclKe	ernel_S	
Thread 66																
▲ COPY 1																
Current Selection Flow Events														Ť		
Slice Details																
Name	ncclKernel_Sen ncclWork*)	dRecv_RING_SIN	IPLE_Sum_int8	_t(ncclDevComm	n*, unsigned long,	Pre S	eceding flows		↗ hipł	ExtLaunchKerne						
Category	null					D	elay		3us							
Start time	30s 277ms 317	us				Т	hread		NULL (C	PU HIP API 2)						
Duration	52ms 383us					Arç	juments									
Thread duration	0s (0.00%)					а	rgs		074400	700066704						
Ihread	2						BeginNs -		274488	/90966/31						
Slice ID	52078						Data +		NULL F22820	50						
	00970						FndNs -	÷	274488	343350681						
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													, _, _, _, _,			

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#### **Comms are important! - RCCL AWS-CXI plugin**

- LUMI, Frontier (and others) directly attaches AMD Instinct<sup>™</sup> MI250x Accelerator to the Slingshot Network
  - Enable collectives computation on devices
  - Minimize the role of the CPU in the control path expose more asynchronous computation opportunities
  - Lowest latency for network message passing is from GPU HBM memory

![](_page_23_Figure_5.jpeg)

- CXI plugin is a runtime dependency. Requires: HPE Cray libfabric implementation
  - https://github.com/rocm/aws-ofi-rccl
  - 3-4x faster collectives
- Included in the LUMI provided containers! If not using the LUMI containers make sure you have that in your environment:

export NCCL\_DEBUG=INFO

export NCCL\_DEBUG\_SUBSYS=INIT

- # and search the logs for:
- [0] NCCL INFO NET/OFI Using aws-ofi-rccl 1.4.0

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## **Configuring RCCL environment (cont.)**

- RCCL should be set configured to use GPU RDMA:
  export NCCL\_NET\_GDR\_LEVEL=PHB
- On upcoming ROCm versions (6.2) this won't be needed – it is default.
- Why should I spend time with all this?
  - 3-4x better bandwidth utilization with plugin
  - 2x better bandwidth utilization with RDMA
  - Can scale further!
- Careful using external containers! You may need to be setting plugin yourself!

![](_page_24_Figure_9.jpeg)

#### Pytorch example app – MNIST distributed learning – Omnitrace

- Obtain more thorough trace information and visualization
  - <u>https://github.com/AMDResearch/omnitrace</u>
- Omnitrace install outside the container can be used
  - The host/container ROCm levels should match

module use module use /appl/local/containers/test-modules module load rocm/6.1.3.lua omnitrace/1.12.0-rocm6.1.x

SIF=/appl/local/containers/sif-images/lumi-pytorch-rocm-6.1.3-python-3.12-pytorch-v2.4.1.sif

- Configuration file:
  - omnitrace-avail -G omnitrace.cfg –all
  - export OMNITRACE\_CONFIG\_FILE=/workdir/omnitrace-config.cfg
  - Override environment with command line arguments if needed

![](_page_25_Picture_12.jpeg)

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## Pytorch example app – MNIST distributed learning – Omnitrace

Let's do sampling!

- Sample learn about the native stack trace along side GPU activity
- GPU activity is never sampled even in sampling mode
- Adjust configuration file according to the needs:

OMNITRACE USE SAMPLING = true OMNITRACE USE ROCM SMI = false OMNITRACE SAMPLING CPUS = none OMNITRACE SAMPLING GPUS = 2

Targeting GPU #2 only used by Rank #2

Execution similar to rocprof:

pcmd=' if [ \$RANK -eq 2 ] ; then pcmd='omnitrace-sample -- '

Prepend omnitrace sampling driver for rank #2

\$pcmd python -u mnist\_DDP.py --gpu --modelpath /workdir/mnist/model

We need to add a few more bindings to singularity: srun --jobid=\$jobid -N \$((Nodes)) -n \$((Nodes\*8)) --gpus \$((Nodes\*8)) --cpu-bind=mask\_cpu:\$MYMASKS \ singularity exec \ Make omnitrace available in the container ...\ -B \$wd:/workdir \ Omnitrace does PCIe info loading – so we need to enable that -B \$OMNITRACE dir/omnitools \ -B /usr/lib64/libpciaccess.so.0 \ \$SIF /workdir/run-me.sh ...and make sure the environment inside the container is set accordingly: Makes sure all Omnitrace bits are available in my environment. export PATH=\$OMNITRACE dir/bin:\$PATH export LD\_LIBRARY\_PATH=\$OMNITRACE\_dir/lib:\$LD\_LIBRARY\_PATH export PYTHONPATH=\$OMNITRACE dir/lib/python/site-packages:\$LD LIBRARY PATH

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![](_page_26_Picture_14.jpeg)

Example 08

Not interested in sampling GPU hardware metrics (frequency, temperature...)

Not interested in sampling CPU hardware metrics

#### Pytorch example app – MNIST distributed learning – Omnitrace

#### • Native stack flame graph:

HIP Activity Device 6, Queue 0			Marker	Ma	Marker	Example 08
HIP Activity Device 6, Queue 1		n	ncclKernel_S	nc	ncclKe	_IKernel_5
HIP Activity Device 6, Queue 65						
HIP Activity Device 6, Queue 66						
HIP Activity Device 6, Queue 67						
			sampl	es [omnitrace]		
o omnitrac	omnitrac o o o o Py_R	omnitrac o o o <b>r</b> om	nnitrace_m omnitrace_main	o o o omnitrace_main o o o.	omnitrac omnitrace_main o o o	o omnitrace_main
P Py_Bytes	Py_Bytes P P P PPyR	Py_Bytes P P P P Py	_BytesMain Py_BytesMain	P P P Py_BytesMain P P P.	Py_Bytes Py_BytesMain P P P	P Py_BytesMain
P Py_RunMa.	Py_RunMaPPPPP	Py_RunMa P P P P	y_RunMain Py_RunMain	P P P P Py_RunMain P P P.	Py_RunMa Py_RunMain P P P	P Py_RunMain
PyRun_A		PyRun_AP	yRun_AnyFPyRun_AnyFileObje	PyRun_AnyFile	PyRun_APyRun_AnyFile	PyRun_AnyFileObject
PyRun_S		PyRun_SPy	yRun_SimpPyRun_SimpleFileO	PyRun_SimpleF	PyRun_SPyRun_SimpleF	PyRun_SimpleFileObject
p pyrun_fi	pyrun_fi p p p p p run_	pyrun_fi p p p p	yrun_file pyrun_file.cold	p p p pyrun_file.cold p p p.	pyrun_fi pyrun_file.cold p p p	p pyrun_file.cold
r run_mod	run_mod r r r r PyEv	run_mod r r r	run_mod run_mod	r r r r run_mod r r r	. run_mod run_mod r r r	r run_mod
r run_eval	run_eval r r r r rPyE	run_eval r r r s rur	n_eval_co run_eval_code_obj	r r r r run_eval_code r r r	. run_eval run_eval_code r r r	r run_eval_code_obj
P PyEval_E	PyEval_E P P P P P	PyEval_E P P P Py	yEval_Eval PyEval_EvalCode	P P P P PyEval_EvalCode P P P.	PyEval_E PyEval_EvalCode P P P	P PyEval_EvalCode
PyEval	_PyEvalPyF/	PyEvalP	yEval_VecPyEval_Vector	PyEval_Vector	PyEvalPyEval_Vector	PyEval_Vector
PyEval	_PyEvalPyE	PyEvalP	yEval_EvaPyEval_EvalFrameD	PyEval_EvalFr	PyEvalPyEval_EvalFr	PyEval_EvalFrameDefault
PyFunct	_PyFunctPy0	PyFunct mP	yFunctionPyFunction_Vector	PyFunction_Ve	PyFunctPyFunction_Ve	PyFunction_Vectorcall
PyEval	_PyEval slot.	PyEvalP	yEval_EvaPyEval_EvalFrameD.	PyEval_EvalFr	PyEvalPyEval_EvalFr	PyEval_EvalFrameDefault
s method_v		🦲 slot_tp s s s mP	yFunction slot_tp_iternext	s s s m slot_tp_iterne s s s.	PyObjec slot_tp_iterne s s s	slot_tp_iternext
v torch::a	vectorca v v v v vPy0/	vectorca v v vP	yEval_Eva vectorcall_method	v v v t vectorcall_met v v v.	slot_tp vectorcall_met v v	vectorcall_method
at::_ops	_PyEvalPyE	PyEval PP	yFunctionPyEval_EvalFrameD	<b>t</b> _PyEval_EvalFr	PyObjecPyEval_EvalFr	PyEval_EvalFrameDefault
_ c10::imp	_PyFunct meth	PyFunct SP	yEval_EvaPyFunction_Vector	aPyFunction_Ve	PyObjecPyFunction_Ve	cPyFunction_Vectorcall
_ at::nati /	_PyEvalPyE	PyEvalP	yFunctionPyEval_EvalFrameD.	<mark>c</mark> _PyEval_EvalFr	PyEvalPyEval_EvalFr	tPyEval_EvalFrameDefault
_ at::_ops	_PyFunct meth	PyFunctP	yEval_EvaPyFunction_Vector	aPyFunction_Ve	method_vPyFunction_Ve	aPyFunction_Vectorcall
c10::imp	_PyEvalPyE	PyEvalcf	unction_vPyEval_EvalFrameD.	aPyEval_EvalFr	PyEvalPyEval_EvalFr	cPyEval_EvalFrameDefault
torch::a	_PyFunct PyOb	PyFunct m t	time_sleepPyFunction_Vector	<b>c</b> _PyFunction_Ve	method_vPyFunction_Ve	aPyFunction_Vectorcall
_ at::_ops	_PyEval slot.	PyEvalP	yTime_FroPyEval_EvalFrameD.	tPyEval_EvalFr	PyEvalPyEval_EvalFr	aPyEval_EvalFrameDefault
s c10::imp	slot_mp s s sPy0	s sP	yTime_Rou slot_mp_subscript	s s s a slot_mp_subscr s s s.	PyObject slot_mp_subscr s s s	c slot_mp_subscript
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at::nati	_PyEval m mPyE		_PyEval_EvalFrameD.	aPyEval_EvalFr	PyObjecPyEval_EvalFr	aPyEval_EvalFrameDefault

![](_page_27_Picture_3.jpeg)

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[Public]

#### Pytorch example app – MNIST distributed learning – Omnitrace

Sampling the Python and C/C++ parts of the code – omnitrace-python

if [ \\$RANK -eq 2 ] ; then omnitrace-python-3.10 -- mnist\_DDP.py --gpu --modelpath /workdir/mnist/model Example 9

Omnitrace expects the Python script as opposed to the Python executable

Match relevant python version python -u mnist DDP.py --gpu --modelpath /workdir/mnist/model

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Mar 7th 2025					Advanced Training								
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else

#### Pytorch example app – MNIST distributed learning – Omniperf

- Obtain detailed kernel performance counters
  - https://github.com/AMDResearch/omniperf

module use module use /appl/local/containers/test-modules module load rocm/6.1.3.lua omniperf/2.1.0

- Virtual environment is used to extend the existing Python environment inside the container.
- Omniperf needs replaying the application many times
  - Could be challenging to profile individual ranks as all need replaying.

![](_page_29_Figure_7.jpeg)

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![](_page_29_Picture_10.jpeg)

![](_page_30_Figure_0.jpeg)

![](_page_31_Figure_0.jpeg)

Pytorch example app – MNIST distributed learning – Omniperf

#### [Public]

Performance (GFLOP/sec)

![](_page_32_Picture_0.jpeg)

#### Pytorch example app – MNIST distributed learning – Rocgdb

- Debugging requires proper driver support
  - Can't run debugger effectively from incompatible containers
  - Use system ROCm for roccgdb
- Two main use cases to use
  - Connecting into a hanging process
  - Progress up to breakpoint or segfault
- ROCm provides rocgdb you may need your own gdbserver.
- Using gdbserver is possible
  - gdbserver can be issued conveniently as a profile tool
  - Launch with:
    - gdbserver --once \$(hostname):12345 ./my\_command
  - Attach with
    - rocgdb -x gdb.commands ./my\_command
  - Leverage gdb commands file to automate startup
    - target remote target\_host:12345

![](_page_33_Figure_16.jpeg)

#### 3 different workflows to choose from!

## If interested in stepping into GPU code you should avoid gdbserver

#### Disclaimer

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Mar 7th 2025

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# **Questions?**